



**Intel PCSD Server Memory Compatibility Test Certificate**

Test System: **Intel S2600WP (Washington Pass)** Test Result: **Pass**

Leveraged System(s): Intel S1600JP (Jackson Pass)

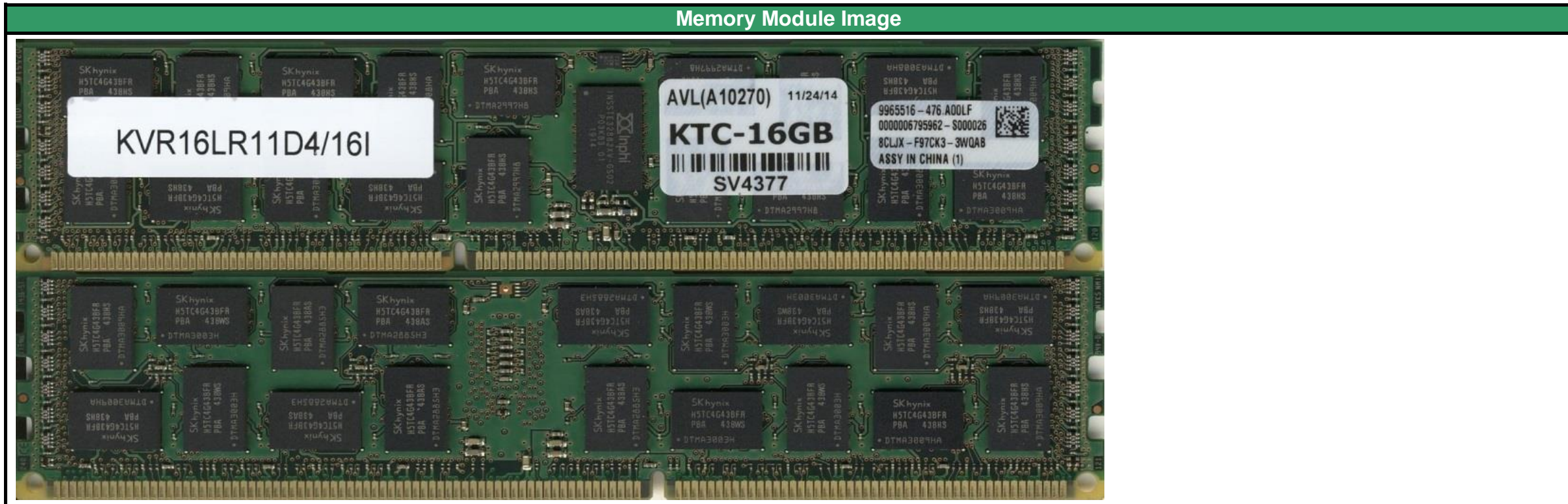
Module Information									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KVR16LR11D4/16I	RDIMM	1.35V	16GB	2Gx72	1600	11	E	DR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.		DIMM Composition		
Hynix	H5TC4G43BFR-PBA	4Gb	1024Mx4bit	1438	Inphi	GS02	(1024Mx4)X2*72		

Leveraged Memory Modules						
Vendor	Type	Voltage	CL	Speed		
1 Kingston	KVR16LR11D4K3/48I	RDIMM	1.35V	11	1600	
2 Kingston	KVR16LR11D4K4/64I	RDIMM	1.35V	11	1600	
3 Kingston	KVR16R11D4/16I	RDIMM	1.5V	11	1600	
4 Kingston	KVR16LR11D4K3/48I	RDIMM	1.5V	11	1600	
5 Kingston	KVR16LR11D4K4/64I	RDIMM	1.5V	11	1600	
6						

System Configuration		
SETUP	System #1	System #2
AVL S/N	SR9654	SU4440
System S/N	QSWP24701947	QSWP14100099
Board Rev. (PBA)	G38670-201	
CPU Type	E5-2690 v2 / 3.0 GHz	
Chipset	Intel C602	
BIOS / Date	02.03.0003 / 04/19/2014	
BMC / ME	01.21.6038 / 02.01.07.328	
FUR/SDR	1.08	
OS	Windows 2008 Enterprise R2 64bit SP1	
Test Tool	iVVSS 2.5.3, SELViewer, Pvmode2, Syscfg, WinPIRA, MemPuller	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD Hot Pass
4. Memory Stress		HVDD Cold Pass
5. Memory Stress		LVDD Hot Pass
6. Memory Stress		LVDD Cold Pass
Note:		



AVL USE ONLY:							
Completed by:	Andy Chang	Completion Date:	01/16/2015	AVL A#	A10270	AVL W/O	WD2647
Comments:							

Test Results

4C					
Minimum Loading					
Start Date		1/7/2015			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	SV4377	P	P	P	P
CPU1 A2					
CPU1 B1	SV4378	P	P	P	P
CPU1 B2					
CPU1 C1	SV4379	P	P	P	P
CPU1 C2					
CPU1 D1	SV4380	P	P	P	P
CPU1 D2					
CPU2 E1	SV4381	P	P	P	P
CPU2 E2					
CPU2 F1	SV4382	P	P	P	P
CPU2 F2					
CPU2 G1	SV4383	P	P	P	P
CPU2 G2					
CPU2 H1	SV4430	P	P	P	P
CPU2 H2					

4C					
Maximum Loading					
Start Date		01/07/15			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	SV4397	P	P	P	P
CPU1 A2	SV4398	P	P	P	P
CPU1 B1	SV4399	P	P	P	P
CPU1 B2	SV4400	P	P	P	P
CPU1 C1	SV4401	P	P	P	P
CPU1 C2	SV4402	P	P	P	P
CPU1 D1	SV4403	P	P	P	P
CPU1 D2	SV4404	P	P	P	P
CPU2 E1	SV4405	P	P	P	P
CPU2 E2	SV4406	P	P	P	P
CPU2 F1	SV4407	P	P	P	P
CPU2 F2	SV4408	P	P	P	P
CPU2 G1	SV4409	P	P	P	P
CPU2 G2	SV4410	P	P	P	P
CPU2 H1	SV4411	P	P	P	P
CPU2 H2	SV4429	P	P	P	P

4C					
Minimum Loading					
Start Date		1/9/2015			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	SV4377	P	P	P	P
CPU1 A2					
CPU1 B1	SV4378	P	P	P	P
CPU1 B2					
CPU1 C1	SV4379	P	P	P	P
CPU1 C2					
CPU1 D1	SV4380	P	P	P	P
CPU1 D2					
CPU2 E1	SV4381	P	P	P	P
CPU2 E2					
CPU2 F1	SV4382	P	P	P	P
CPU2 F2					
CPU2 G1	SV4383	P	P	P	P
CPU2 G2					
CPU2 H1	SV4384	P	P	P	P
CPU2 H2					

4C					
Maximum Loading					
Start Date		01/09/15			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	SV4397	P	P	P	P
CPU1 A2	SV4398	P	P	P	P
CPU1 B1	SV4399	P	P	P	P
CPU1 B2	SV4400	P	P	P	P
CPU1 C1	SV4401	P	P	P	P
CPU1 C2	SV4402	P	P	P	P
CPU1 D1	SV4403	P	P	P	P
CPU1 D2	SV4404	P	P	P	P
CPU2 E1	SV4405	P	P	P	P
CPU2 E2	SV4406	P	P	P	P
CPU2 F1	SV4407	P	P	P	P
CPU2 F2	SV4408	P	P	P	P
CPU2 G1	SV4409	P	P	P	P
CPU2 G2	SV4410	P	P	P	P
CPU2 H1	SV4411	P	P	P	P
CPU2 H2	SV4412	P	P	P	P