

**Server Memory Compatibility Test Certificate**

Test System: <b>Supermicro X9SRE-F</b>	Test Result: <b>Pass</b>
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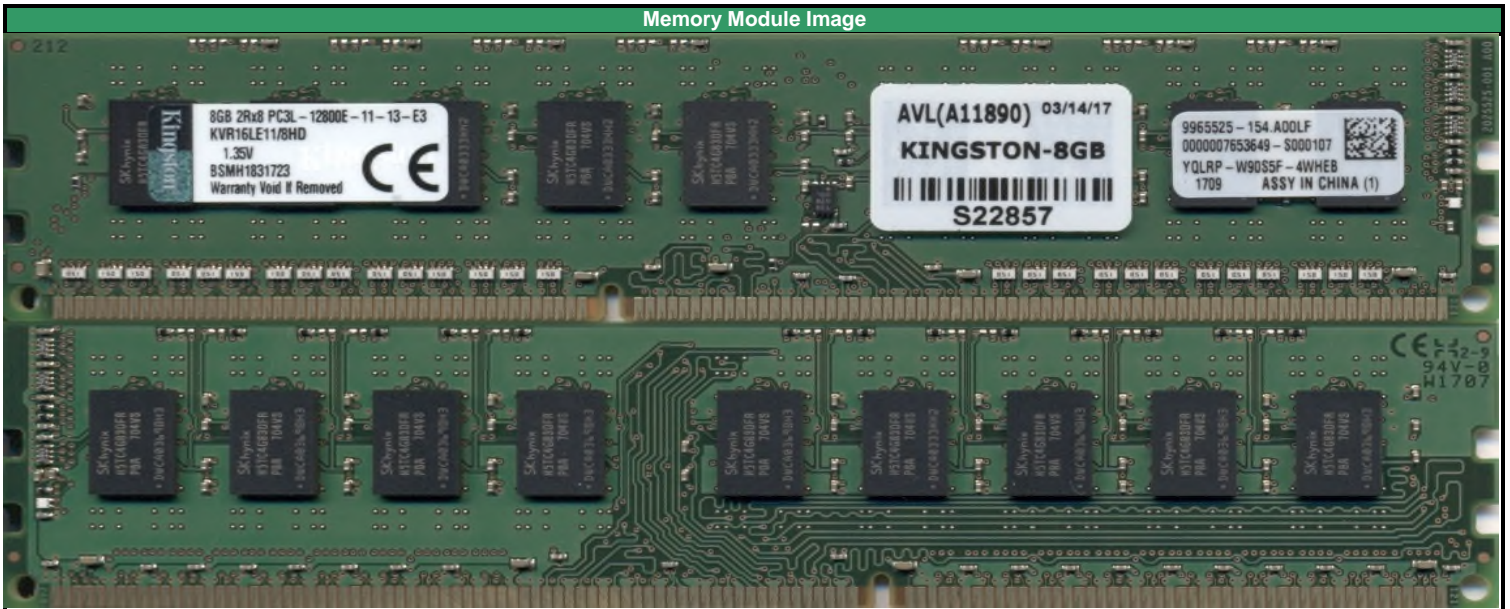
Leveraged System(s): X9SRA, X9SRi, X9SRI-F, X9SRE, X9SRG-F, X9SRW-F, X9SRW-3F

Module Specifications										Rev. 1/13/2015
<b>DIMM Vendor</b>	<b>DIMM Part Number</b>	<b>Type</b>	<b>Voltage</b>	<b>Size</b>	<b>Config.</b>	<b>Speed</b>	<b>CL</b>	<b>R/C</b>	<b>Rank</b>	
Kingston	KVR16LE11-8HD	UDIMM ECC	1.35V	8GB	1Gx72	1600	11	E	DR	
<b>DRAM Vendor</b>	<b>DRAM Part Number</b>	<b>DRAM Density / Width / Date Code</b>			<b>Register Vendor / Rev.</b>		<b>DIMM Composition</b>			
Sk Hynix	H5TC4G83DFR-PBA	4Gb	512Mx8bit	1704	N/A	N/A	(1024Mx8)*72			

System Configuration	
SETUP	System Information
AVL S/N	SO6530
System S/N	ZM23U39156
Board Rev. (PBA)	1.2
CPU Type	Intel E5-2650(IV) x 1
Chipset	Intel C602
BIOS / Date	3.2 /8/31/2015
BMC / ME	2.14
FRU/SDR	n/a
OS	Red Hat Enterprise Linux 6.2 Kernel 2.6.32
Test Tool	SAT, Memory Stress v1077

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	<b>Done</b>
2. SPD Check	DIMM SPD content check for JEDEC compliance	<b>Pass</b>
3. Reset Cycle	1 DIMM Per Channel when applicable	<b>Pass</b>
4a. Memory Stress	DIMM Loading per spec	8 hours per config
4b. Memory Benchmark		5 loops per config
4c. Reset Cycle		200 loops per config
5. Functional Stress Test		12 hours
		<b>Pass</b>

Note:



AVL USE ONLY:					
Completed by:	Van Nguyen	Completion Date:	3/23/2017	AVL A#	11890
				AVL W/O	WD7704
Comments:					