

**Server Memory Compatibility Test Certificate**

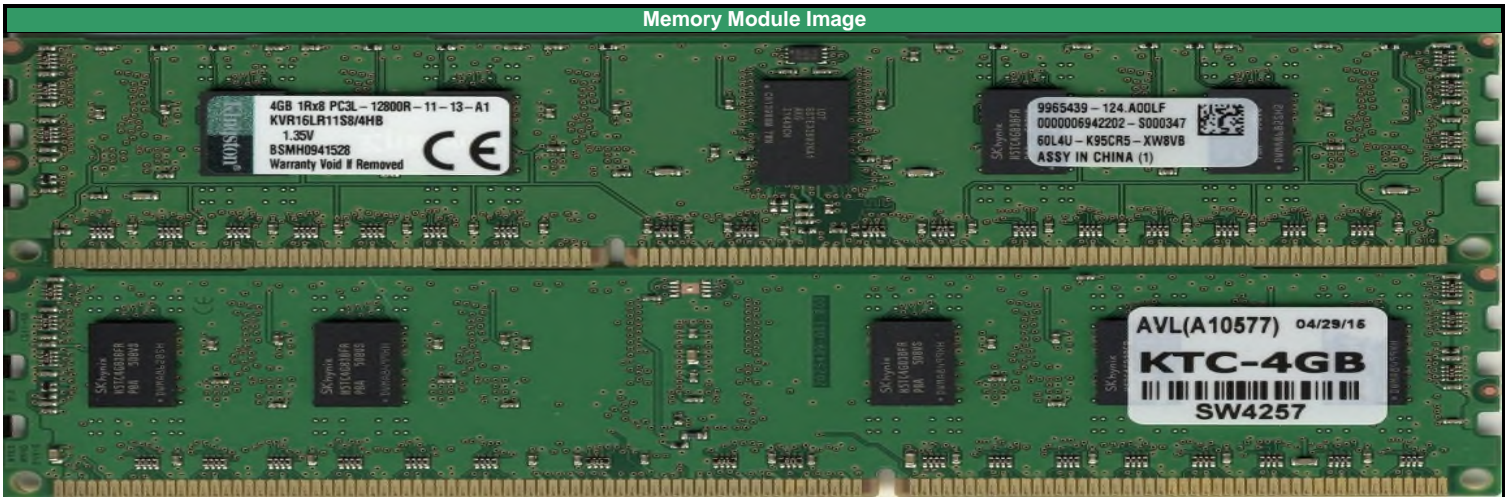
Test System: **Supermicro X8DTN-F** Test Result: **Pass**

Leveraged System(s): X8DTN-F

Module Specifications										Rev. 1/13/2015
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank	
Kingston	KVR16LR11S8/4HB	RDIMM	1.35V	4GB	512Mx72	1600	11	A	SR	
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.		DIMM Composition			
Sk Hynix	H5TC4G83BFR-PBA	4Gb	512Mx8bit	1508	IDT	6.3	(512Mx8)*72			

System Configuration	
SETUP	System Information
AVL S/N	SM9029
System S/N	1234567890
Board Rev. (PBA)	2.0
CPU Type	Intel X5650(WSM) x 2
Chipset	Intel 5520 (Tylersburg), Rev C2
BIOS / Date	2.1c / 10/28/11
BMC / ME	1.33
FRU/SDR	n/a
OS	Red Hat Enterprise Linux 6.2 Kernel 2.6.32
Test Tool	SAT, Memory Stress v1077

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Reset Cycle	1 DIMM Per Channel when applicable	Pass
4a. Memory Stress	DIMM Loading per spec	8 hours per config
4b. Memory Benchmark		5 loops per config
4c. Reset Cycle		200 loops per config
5. Functional Stress Test		12 hours
Note:		



AVL USE ONLY:							
Completed by:	Van Nguyen	Completion Date:	5/11/2015	AVL A#	10577	AVL W/O	WD3518
Comments:							