

**Server Memory Compatibility Test Certificate**

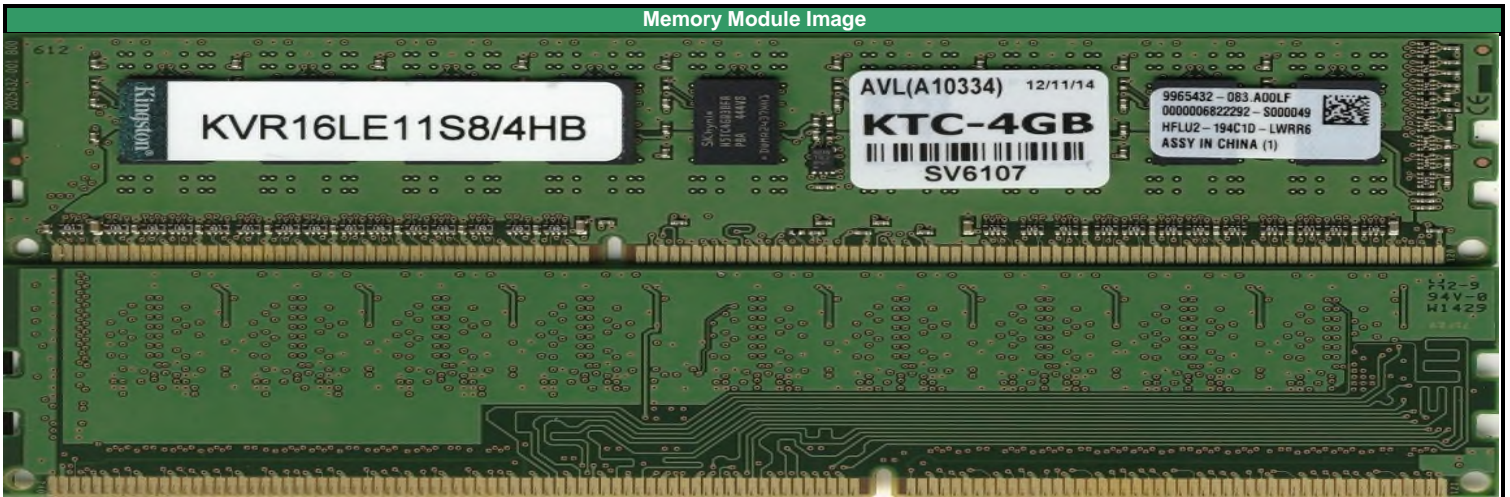
Test System: **Supermicro X9DRL-IF** Test Result: **Pass**

Leveraged System(s): X9DRL-3F, X9DRD-IF, X9DRT-HF, X9DRT-HF+, X9DRT-HIBQF, X9DRT-HIBFF, X9DRG-HF, X9DRG-HTF

Module Specifications										Rev. 1/13/2015
<b>DIMM Vendor</b>	<b>DIMM Part Number</b>	<b>Type</b>	<b>Voltage</b>	<b>Size</b>	<b>Config.</b>	<b>Speed</b>	<b>CL</b>	<b>R/C</b>	<b>Rank</b>	
Kingston	KVR16LE11S8/4HB	UDIMM ECC	1.35V	4GB	512Mx72	1600	11	D	SR	
<b>DRAM Vendor</b>	<b>DRAM Part Number</b>	<b>DRAM Density / Width / Date Code</b>			<b>Register Vendor / Rev.</b>		<b>DIMM Composition</b>			
Hynix	H5TC4G83BFR-PBA	4Gb 512Mx8bit 1444			n/a n/a		(512X8)*72			

System Configuration	
SETUP	System Information
AVL S/N	SN1029
System S/N	WM23S30658
Board Rev. (PBA)	1234567890
CPU Type	Intel E5-2650(V2) x 2
Chipset	Intel C602
BIOS / Date	3.0a / 08/08/2013
BMC / ME	2.2
FRU/SDR	n/a
OS	Red Hat Enterprise Linux 6.2 Kernel 2.6.32
Test Tool	SAT, Memory Stress v1077

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Reset Cycle	1 DIMM Per Channel when applicable	N/A
4a. Memory Stress	DIMM Loading per spec	8 hours per config
4b. Memory Benchmark		5 loops per config
4c. Reset Cycle		200 loops per config
5. Functional Stress Test		12 hours
Note:		



AVL USE ONLY:							
Completed by:	Van Nguyen	Completion Date:	1/19/2015	AVL A#	10334	AVL W/O	WD2879
Comments:							