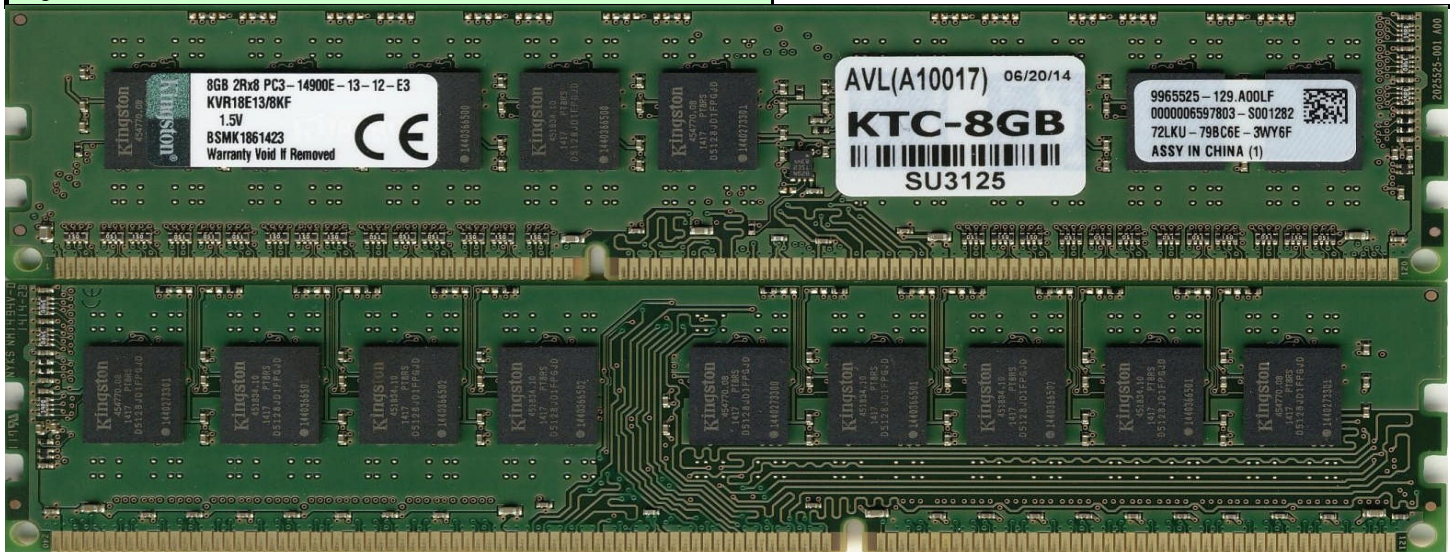
	AVL Supermicro server platform Memory Module Qualification Test		
	Intel E5-2650(IVB) x 1, Intel C602	Test Results	Pass
	PN: KVR18E13/8KF (8GB / UDIMM ECC / ECC) On: X9SRE-F Rev.1.2		

RP77D3x-106-KI-SQ-SMC-V2	Module Information	Rev 05/30/2012
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AVL WorkOrder #	WD1730	AVL A#	10017
Start Date	6/26/2014	End Date	6/27/2014
Tested By	Van N.		
Module Manufacturer	Kingston		
Module Part Number	KVR18E13/8KF		
Module BOM Number	9965525-129.A00LF		
Module Capacity / Memory Type / ECC	8GB / UDIMM ECC / ECC		
Module Configuration (Width, # of devices, # of Ranks)	1Gx72 /18 Devices / 2 Ranks		
Speed Tested (Data rate of Mbps, CL-tRP-tRCD)	DDR3-1866 /13-13-13		
DRAM Device Vendor	Kingston		
DRAM Device Part Number / Date code	D5128JD1FPGJD		1417
DRAM Die Revision / Process Technology (nm)	F		
DRAM Device Config (Density / Width)	512Mbit / x8		/512Mx8bit
Thermal Sensor Device Vendor / Part Number / Revision	STMicro		
Register Device Vendor / Part Number / Revision	N/A	N/A	N/A



Platform System Information				
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Motherboard Info (Model# & MB Revision & MB S/N & AVL S/N)	X9SRE-F	1.2	ZM23U39156	SO6530
BIOS Revision / BIOS Date	3.0		10/23/2013	
CPU / Speed	Intel E5-2650(IVB) x 1		2.6GHz	
Chipset info (Stepping)	Intel C602			



AVL Supermicro server platform Memory Module Qualification Test

Intel E5-2650(IVB) x 1, Intel C602

PN: KVR18E13/8KF (8GB / UDIMM ECC / ECC) On: X9SRE-F Rev.1.2

Test Results:

PASS

Comments:

AVL Memory Module Qual Test Results Summary

Test # and name	Test Description	Specs	Test	Comments
			Results	
1. Latest BIOS Upgrade & Configuration	Download / Upgrade latest BIOS & record size and speed detection		Done	
2. SPD Check	Memory module SPD content check for JEDEC compliance	JEDEC	Pass	Use proprietary tools
3. Reset Cycle	Run Linux based diags & utility software @ 55°C	50 loops	Pass	1 DIMM Per Channel when applicable
4a. Stress Application Test	Run Linux based diags & utility software @55°C	8 Hour per config	Pass	DIMM Loading per spec
4b. Stream Benchmark Test		5 loop per config	Pass	DIMM Loading per spec
4b. Reset Cycle		200 loop per config	Pass	DIMM Loading per spec
5. Functional Stress Test	Memory Stress Test @55°C	12hrs	Pass	DIMM Loading per spec
6. Stress Application Test	Run Linux based diags & utility software @55°C	8hrs	N/A	3 DIMM Per Channel when applicable

Note: All test under IMC Vdd=Nom, Vref=Vddnom/2