

	<b>AVL Supermicro server platform Memory Module Qualification Test</b>		
	Intel X5650(WSM) x 2, Intel 5520 (Tylersburg), Rev C2	Test Results	<b>Pass</b>
	PN: KVR16LE11S8/4KF (4GB / UDIMM / ECC ) On: X8DTT-F Rev.1.02		

RP77D3x-128-KI-SQ-SMC-V2		Module Information		Rev 04/25/2013
AVL WorkOrder #	WC9854	AVL A#	9768	
Start Date	2/6/2014	End Date	2/7/2014	
Tested By	Van N.			
Module Manufacturer	Kingston			
Module Part Number	KVR16LE11S8/4KF			
Module BOM Number	9965432-069.A00LF			
Module Capacity / Memory Type / ECC	4GB / UDIMM / ECC			
Module Configuration (Width, # of devices, # of Ranks)	512Mx72 /9 Devices / 1 Rank			
Module Speed (Data rate of Mbps, CL-tRP-tRCD)	DDR3L-1600 /11-11-11			
DRAM Device Vendor	Kingston			
DRAM Device Part Number / Date code	D5128ED1FPGGB			1352
DRAM Die Revision / Process Technology ( nm )	F			
DRAM Device Config (Density / Width)	512Mbit / x8 /512Mx8bit			
Thermal Sensor Device Vendor / Part Number / Revision	On-Semi			
Register Device Vendor / Part Number / Revision	N/A			



Platform System Information				
Motherboard Info (Model# & MB Revision & MB S/N & AVL S/N)	X8DTT-F	1.02	OM03S39747	SK4376
BISO Revision / BIOS Date / MRC Rev.	2.1b	10/28/2011		
CPU / Speed	Intel X5650(WSM) x 2		2.66GHz	
Chipset info (Stepping)	Intel 5520 (Tylersburg), Rev C2			



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**Test Results:**

**PASS**

Comments:

### AVL Memory Module Qual Test Results Summary

Test # and name	Test Description	Specs	Test Results	Comments
<b>1. Latest BIOS Upgrade &amp; Configuration</b>	Download / Upgrade latest BIOS & record size and speed detection	Per test platform, DIMM & config spec	<b>Done</b>	Record memory size & speed at each test only
<b>2. SPD Check</b>	Memory module SPD content check for JEDEC compliance	JEDEC	<b>Pass</b>	Use proprietary tools
<b>3. Reset Cycle</b>	Run Linux based diags & utility software @55°C	50 loops	<b>Pass</b>	1 DIMM Per Channel when applicable
<b>4a. Stress Application Test</b>	Run Linux based diags & utility software @55°C	8 Hour per config	<b>Pass</b>	DIMM Loading per spec
<b>4b. Stream Benchmark Test</b>		5 loop per config	<b>Pass</b>	DIMM Loading per spec
<b>4b. Reset Cycle</b>		200 loop per config	<b>Pass</b>	DIMM Loading per spec
<b>5. Functional Stress Test</b>	Memory Stress Test @55°C	12hrs	<b>Pass</b>	DIMM Loading per spec
<b>6. Stress Application Test</b>	Run Linux based diags & utility software @55°C	8hrs	<b>N/A</b>	3 DIMM Per Channel when applicable

Note: All test under IMC Vdd=Nom, Vref=Vddnom/2