

	AVL Supermicro server platform Memory Module Qualification Test		
	Intel X5650(WSM) x 2, Intel 5520 (Tylersburg), Rev C2	Test Results	Pass
	PN: KVR16LR11S8/4KF (4GB / RDIMM / ECC) On: X8DAH+ Rev.2.01		

RP77D3x-128-KI-SQ-SMC-V2		Module Information		Rev 04/25/2013
AVL WorkOrder #	WC9569	AVL A#	9645	
Start Date	2/20/2014	End Date	2/27/2014	
Tested By	Van N.			
Module Manufacturer	Kingston			
Module Part Number	KVR16LR11S8/4KF			
Module BOM Number	9965439-092.A00LF			
Module Capacity / Memory Type / ECC	4GB / RDIMM / ECC			
Module Configuration (Width, # of devices, # of Ranks)	512Mx72 /9 Devices / 1 Rank			
Module Speed (Data rate of Mbps, CL-tRP-tRCD)	DDR3L-1600 /11-11-11			
DRAM Device Vendor	Kingston			
DRAM Device Part Number / Date code	D5128ED1FPGGB	1345		
DRAM Die Revision / Process Technology (nm)	F			
DRAM Device Config (Density / Width)	4Gbit / x8 512MX8bit			
Thermal Sensor Device Vendor / Part Number / Revision	On-Semi			
Register Device Vendor / Part Number / Revision	Montage	M88SSTE32882H0	1.0	



Platform System Information				
Motherboard Info (Model# & MB Revision & MB S/N & AVL S/N)	X8DAH+	2.01	VM09S54454	SK4373
BISO Revision / BIOS Date / MRC Rev.	2.1	12/30/2012		
CPU / Speed	Intel X5650(WSM) x 2		2.66GHz	
Chipset info (Stepping)	Intel 5520 (Tylersburg), Rev C2			



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Test Results:

PASS

Comments:

AVL Memory Module Qual Test Results Summary

Test # and name	Test Description	Specs	Test Results	Comments
1. Latest BIOS Upgrade & Configuration	Download / Upgrade latest BIOS & record size and speed detection	Per test platform, DIMM & config spec	Done	Record memory size & speed at each test only
2. SPD Check	Memory module SPD content check for JEDEC compliance	JEDEC	Done	Use proprietary tools
3. Reset Cycle	Run Linux based diags & utility software @55°C	50 loops	Pass	1 DIMM Per Channel when applicable
4a. Stress Application Test	Run Linux based diags & utility software @55°C	8 Hour per config	Pass	DIMM Loading per spec
4b. Stream Benchmark Test		5 loop per config	Pass	DIMM Loading per spec
4b. Reset Cycle		200 loop per config	Pass	DIMM Loading per spec
5. Functional Stress Test	Memory Stress Test @55°C	12hrs	Pass	DIMM Loading per spec
6. Stress Application Test	Run Linux based diags & utility software @55°C	8hrs	Pass	3 DIMM Per Channel when applicable

Note: All test under IMC Vdd=Nom, Vref=Vddnom/2