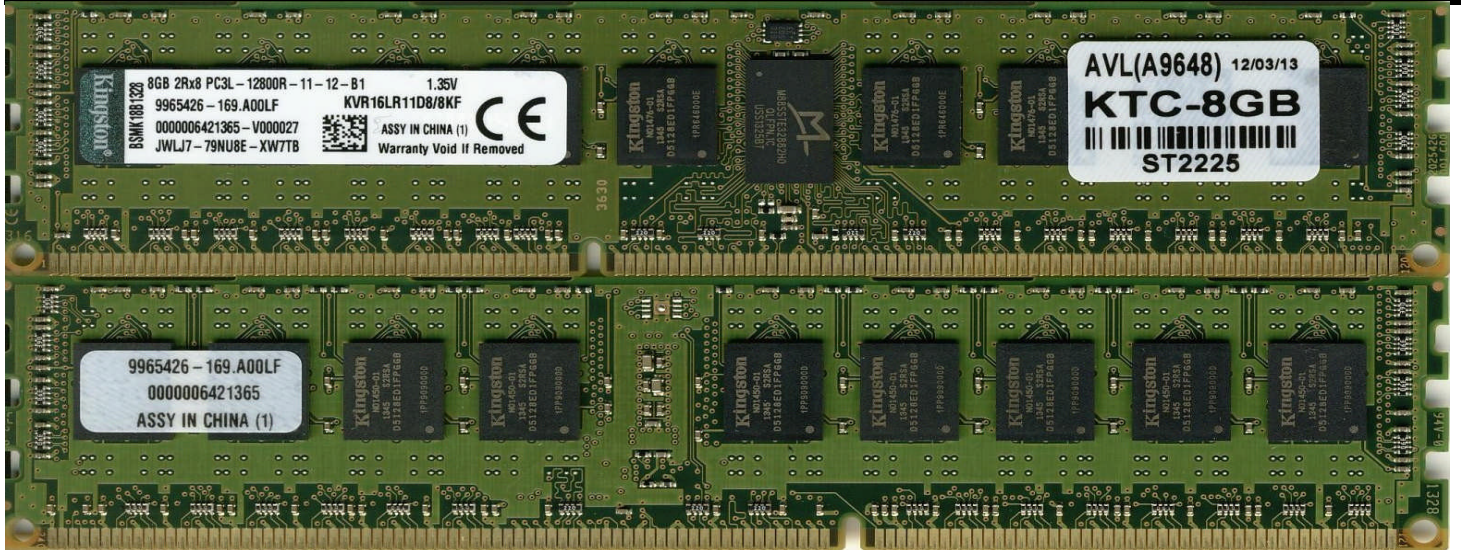


	AVL Supermicro server platform Memory Module Qualification Test		
	Intel E5-2650(IVB) x 2, Intel C602	Test Results	Pass
	PN: KVR16LR11D8/8KF (8GB / RDIMM / ECC) On: X9DRL-iF Rev.1234567890		

RP77D3x-106-KI-SQ-SMC-V2		Module Information		Rev 05/30/2012
AVL WorkOrder #	WC9368	AVL A#	9648	
Start Date	12/13/2016	End Date	12/16/2013	
Tested By	Van N.			
Module Manufacturer	Kingston			
Module Part Number	KVR16LR11D8/8KF			
Module BOM Number	9965426-169.A00LF			
Module Capacity / Memory Type / ECC	8GB / RDIMM / ECC			
Module Configuration (Width, # of devices, # of Ranks)	1Gx72 /18 Devices / 2 Ranks			
Module Speed (Data rate of Mbps, CL-tRP-tRCD)	DDR3L-1600 /11-11-11			
DRAM Device Vendor	Kingston			
DRAM Device Part Number / Date code	D5128ED1FPGG6			
DRAM Die Revision / Process Technology (nm)	F			
DRAM Device Config (Density / Width)	512Mbit / x8 /512Mx8bit			
Thermal Sensor Device Vendor / Part Number / Revision	On-Semi			
Register Device Vendor / Part Number / Revision	Montage	SSTE32882	1.0	



Platform System Information				
Motherboard Info (Model# & MB Revision & MB S/N & AVL S/N)	X9DRL-iF	1234567890	WM23S30692	SN1029
BIOS Revision / BIOS Date	3.0		8/8/2013	
CPU / Speed	Intel E5-2650(IVB) x 2		2.6GHz	
Chipset info (Stepping)	Intel C602			


AVL Supermicro server platform Memory Module Qualification Test

Intel E5-2650(IVB) x 2, Intel C602

PN: KVR16LR11D8/8KF (8GB / RDIMM / ECC) On: X9DRL-iF Rev.1234567890

Test Results:**PASS***Comments:*
AVL Memory Module Qual Test Results Summary

Test # and name	Test Description	Specs	Test Results	Comments
1. Latest BIOS Upgrade & Configuration	Download / Upgrade latest BIOS & record size and speed detection		Done	
2. SPD Check	Memory module SPD content check for JEDEC compliance	JEDEC	Pass	Use proprietary tools
3. Reset Cycle	Run Linux based diags & utility software @ 55°C	50 loops	N/A	1 DIMM Per Channel when applicable
4a. Stress Application Test	Run Linux based diags & utility software @55°C	8 Hour per config	Pass	DIMM Loading per spec
4b. Stream Benchmark Test		5 loop per config	Pass	DIMM Loading per spec
4b. Reset Cycle		200 loop per config	Pass	DIMM Loading per spec
5. Functional Stress Test	Memory Stress Test @55°C -	12hrs	Pass	DIMM Loading per spec
6. Stress Application Test	Run Linux based diags & utility software @55°C	8hrs	N/A	3 DIMM Per Channel when applicable

Note: All test under IMC Vdd=Nom, Vref=Vddnom/2