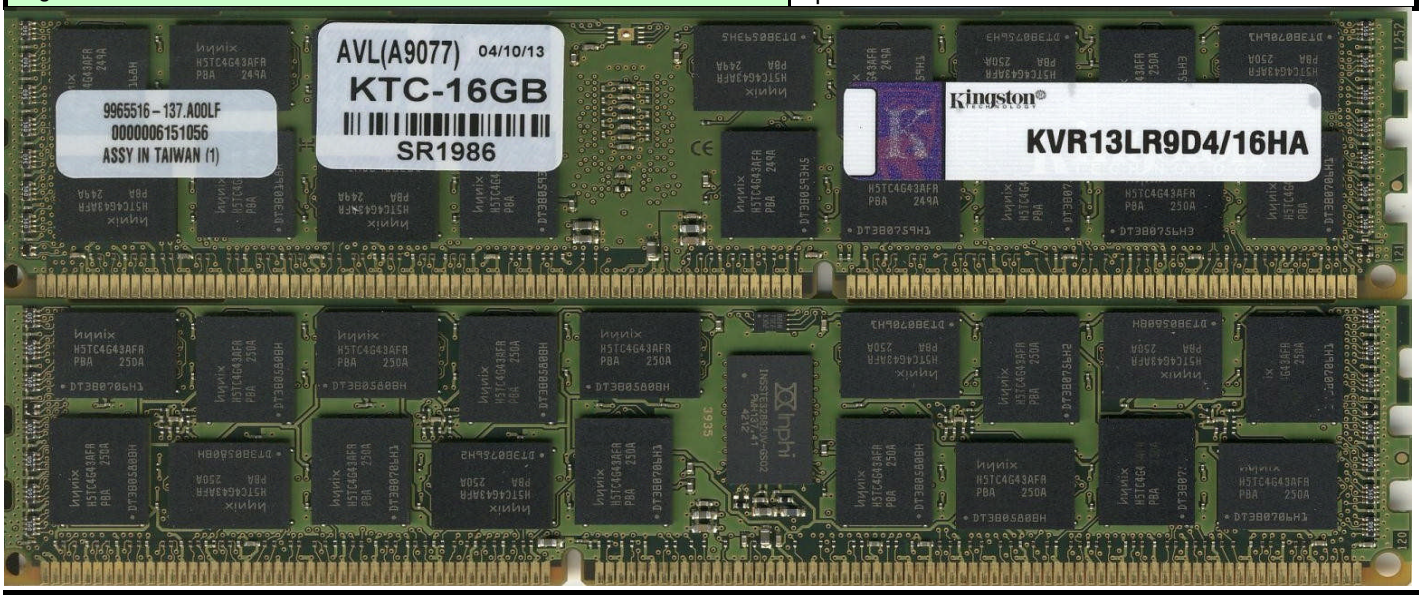
AVL Supermicro server platform Memory Module Qualification Test

AMD Opteron 6128 x1, AMD SR5690 CIMx 1.0.1.0	Test Results	Pass
PN: KVR13LR9D4/16HA (16GB / RDIMM / ECC) On: H8SGL-F Rev.1.00		

Module Information

RP77D3x-128-KI-SQ-SMC-V2 — Rev 04/25/2013

AVL WorkOrder #	WC6820	AVL A#	9077
Start Date	6/18/2013	End Date	6/20/2013
Tested By		Van N.	
Module Manufacturer		Kingston	
Module Part Number		KVR13LR9D4/16HA	
Module BOM Number		9965516-137.A00LF	
Module Capacity / Memory Type / ECC		16GB / RDIMM / ECC	
Module Configuration (Width, # of devices, # of Ranks)		2Gx72 /36 Devices / 2 Ranks	
Module Speed (Data rate of Mbps, CL-tRP-tRCD)		DDR3L-1333 /9-9-9	
DRAM Device Vendor		Hynix	
DRAM Device Part Number / Date code		H5TC4G43AFR-PCA	1250
DRAM Die Revision / Process Technology (nm)		A	
DRAM Device Config (Density / Width)		4Gbit / x4 /1024MX4bit	
Thermal Sensor Device Vendor / Part Number / Revision		STMicro	
Register Device Vendor / Part Number / Revision		Inphi INSSTE32882UV-GS02	2.1



Platform System Information

Motherboard Info (Model# & MB Revision & MB S/N & AVL S/N)	H8SGL-F	1.00	OM12S7013	SL2697
BISO Revision / BIOS Date / MRC Rev.	2.0a	11/11/2011		
CPU / Speed	AMD Opteron 6128 x1		2.0GHz	
Chipset info (Stepping)	AMD SR5690 CIMx 1.0.1.0			

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Test Results:	PASS
Commetnts:	

AVL Memory Module Qual Test Results Summary

Test # and name	Test Description	Specs	Test Results	Comments
1. Latest BIOS Upgrade & Configuration	Download / Upgrade latest BIOS & record size and speed detection	Per test platform, DIMM & config spec	**Done**	Record memory size & speed at each test only
2. SPD Check	Memory module SPD content check for JEDEC compliance	JEDEC	**Pass**	Use proprietary tools
3. Reset Cycle	Run Linux based diags & utility software @55°C	50 loops	**Pass**	1 DIMM Per Channel when applicable
4a. Stress Application Test	Run Linux based diags & utility software @55°C	8 Hour per config	**Pass**	DIMM Loading per spec
4b. Stream Benchmark Test		5 loop per config	**Pass**	DIMM Loading per spec
4b. Reset Cycle		200 loop per config	**Pass**	DIMM Loading per spec
5. Functional Stress Test	Memory Stress Test @55°C	12hrs	**Pass**	DIMM Loading per spec
6. Stress Application Test	Run Linux based diags & utility software @55°C	8hrs	**N/A**	3 DIMM Per Channel when applicable

Note: All test under IMC Vdd=Nom, Vref=Vddnom/2