



Advanced Validation Labs, Inc.

17665B Newhope Street, Fountain Valley, CA 92708 (714) 435-2630



Intel PCSD Server Memory Compatibility Test Certificate	
Test System: Intel S1200SP (Silver Pass)	Test Result: Pass

Leveraged System(s): S1200SPL; S1200SPO; S1200SPS

Modules Information									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KVR21E15D8/16l	UDIMM ECC	1.2V	16GB	2Gx72	2133	15	E	DR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.		DIMM Composition		
Micron	MT40A1G8PM-083E:A	8Gb	1024Mx8bit	1614				(1024Mx8)*2*72	

Leveraged Memory Modules						
Vendor	Type	Voltage	CL	Speed		
1 Kingston	KVR21E15D8K2/32l	UDIMM ECC	1.2V	15	2133	
2						
3						
4						
5						
6						

System Configuration		
SETUP	System #1	System #2
AVL S/N	SY9443	SY9444
System S/N	QSSA54400007 / LVPP	QSSA54400010 / HVPP
Board Rev. (PBA)	H57532-208	
CPU Type	E3-1280 / 3.70 GHz	
Chipset	C232	
BIOS / Date	01.03.0006 / 3-17-2016	
BMC / ME	01.02.9655 / 04.00.03.94	
FUR/SDR	0.19	
OS	Windows Server 2012 R2	
Test Tool	iWVSS 2.6.4, SELViewer, Syscfg, WinPIRA	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD/HVPP Hot
4. Memory Stress		HVDD/HVPP Cold
5. Memory Stress		LVDD/LVPP Hot
6. Memory Stress		LVDD/LVPP Cold
Note:		

Memory Module Image



AVL USE ONLY:							
Completed by:	Andy Chang	Completion Date:	05/31/2016	AVL A#	A11307	AVL W/O	WD5947
Comments:							

Test Results

4C						
Minimum Loading						
Start Date		5/23/2016				
DIMM Voltage		1.22v / 1.16v				
DIMM VPP		2.64v / 2.422v				
DIMM	S/N	A	B	C	D	
CPU1 A1	SZ1040	P	P	P	P	
CPU1 A2						
CPU1 B1	SZ1041	P	P	P	P	
CPU1 B2						

4C						
Maximum Loading						
Start Date		05/23/16				
DIMM Voltage		1.22v / 1.16v				
DIMM VPP		2.64v / 2.422v				
DIMM	S/N	A	B	C	D	
CPU1 A1	SZ1036	P	P	P	P	
CPU1 A2	SZ1037	P	P	P	P	
CPU1 B1	SZ1038	P	P	P	P	
CPU1 B2	SZ1039	P	P	P	P	

