



Intel PCSD Server Memory Compatibility Test Certificate	
Test System: Intel S2400LP (Lincoln Pass)	Test Result: Pass

Leveraged System(s): Intel S2400BB (Black Bear Pass), Intel S1400FP (Freemont Pass), Intel S1400(Salmon Pass)

Module Information									
Rev. 03/01/2014									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KVR16LE11S8/4I	UDIMM ECC	1.35V	4GB	512Mx72	1600	11	D	SR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.		DIMM Composition		
Hynix	H5TC4G83BFR-PBA	4Gb	512Mx8bit	1444			(512Mx8)*72		

Leveraged Memory Modules						
Vendor	Type	Voltage	CL	Speed		
1 Kingston	KVR16E11S8/4I	UDIMM ECC	1.5V	11	1600	
2						
3						
4						
5						
6						

System Configuration		
SETUP	System #1	System #2
AVL S/N	SO7932	SR7541
System S/N	AZLP15100088	AZLP23700462
Board Rev. (PBA)	G18581-201	G18580-204
CPU Type	E5-2450 v2 / 2.1 GHz	
Chipset	Intel C602	
BIOS / Date	02.03.0003 / 04/19/2014	
BMC / ME	01.21.6038 / 02.01.07.328	
FUR/SDR	1.09	
OS	Windows 2008 Enterprise R2 64bit SP1	
Test Tool	iWVSS 2.5.3, SELViewer, Pvmode2, Syscfg, WinPIRA, MemPuller	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD Hot Pass
4. Memory Stress		HVDD Cold Pass
5. Memory Stress		LVDD Hot Pass
6. Memory Stress		LVDD Cold Pass
Note:		

Memory Module Image	
AVL USE ONLY:	
Completed by: Andy Chang	Completion Date: 02/04/15
AVL A# A10338	AVL W/O WD2913
Comments:	

Test Results

4C					
Minimum Loading					
Start Date		01/31/15			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	SV6339	P	P	P	P
CPU1 A2					
CPU1 B1	SV6340	P	P	P	P
CPU1 B2					
CPU1 C1	SV6341	P	P	P	P
CPU1 C2					
CPU1 D1	SV6342	P	P	P	P
CPU1 D2					
CPU2 E1	SV6343	P	P	P	P
CPU2 E2					
CPU2 F1	SV6207	P	P	P	P
CPU2 F2					

4C					
Maximum Loading					
Start Date		01/29/15			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	SV6339	P	P	P	P
CPU1 A2	SV6340	P	P	P	P
CPU1 B1	SV6341	P	P	P	P
CPU1 B2	SV6342	P	P	P	P
CPU1 C1	SV6343	P	P	P	P
CPU1 C2	SV6344	P	P	P	P
CPU1 D1	SV6345	P	P	P	P
CPU1 D2	SV6346	P	P	P	P
CPU2 E1	SV6347	P	P	P	P
CPU2 E2	SV6348	P	P	P	P
CPU2 F1	SV6349	P	P	P	P
CPU2 F2	SV6207	P	P	P	P



4C					
Minimum Loading					
Start Date		02/01/15			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	SV6339	P	P	P	P
CPU1 A2					
CPU1 B1	SV6340	P	P	P	P
CPU1 B2					
CPU1 C1	SV6341	P	P	P	P
CPU1 C2					
CPU1 D1	SV6342	P	P	P	P
CPU1 D2					
CPU2 E1	SV6343	P	P	P	P
CPU2 E2					
CPU2 F1	SV6344	P	P	P	P
CPU2 F2					

4C					
Maximum Loading					
Start Date		01/28/15			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	SV6339	P	P	P	P
CPU1 A2	SV6340	P	P	P	P
CPU1 B1	SV6341	P	P	P	P
CPU1 B2	SV6342	P	P	P	P
CPU1 C1	SV6343	P	P	P	P
CPU1 C2	SV6344	P	P	P	P
CPU1 D1	SV6345	P	P	P	P
CPU1 D2	SV6346	P	P	P	P
CPU2 E1	SV6347	P	P	P	P
CPU2 E2	SV6348	P	P	P	P
CPU2 F1	SV6349	P	P	P	P
CPU2 F2	SV6350	P	P	P	P

