



### Intel PCSD Server Memory Compatibility Test Certificate

Test System: **Intel S2600GZ (Grizzly Pass)**

Test Result: **Pass**

Leveraged System(s):N/A

Module Information									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KVR16LR11S4/8I	RDIMM	1.35V	8GB	1Gx72	1600	11	C	SR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.		DIMM Composition		
Hynix	H5TC4G43BFR-PBA	4Gb	1024Mx4bit	1438	Inphi	GS02	(1024Mx4)*72		

Leveraged Memory Modules						
Vendor	Type	Voltage	CL	Speed		
1 Kingston	KVR16LR11S4K3/24I	RDIMM	1.35V	11	1600	
2 Kingston	KVR16LR11S4K4/32I	RDIMM	1.35V	11	1600	
3 Kingston	KVR16R11S4/8I	RDIMM	1.5V	11	1600	
4 Kingston	KVR16R11S4K3/24I	RDIMM	1.5V	11	1600	
5 Kingston	KVR16R11S4K4/32I	RDIMM	1.5V	11	1600	
6						

System Configuration		
SETUP	System #1	System #2
AVL S/N	SR2525	SO8251
System S/N	QSGR14500317	QSGR14600736
Board Rev. (PBA)	G11481-301	
CPU Type	E5-2697 v2 / 2.7 GHz	
Chipset	Intel C602	
BIOS / Date	02.03.2003 / 06/19/2014	
BMC / ME	1.19.5018 / 02.01.07.231	
FUR/SDR	1.13	
OS	Windows 2008 Enterprise R2 64bit SP1	
Test Tool	iWVSS 2.5.3, SELViewer, Pvmode2, Syscfg, WinPIRA,MemPuller	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	<b>Done</b>
2. SPD Check	DIMM SPD content check for JEDEC compliance	<b>Pass</b>
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD Hot - A/E <b>Pass</b>
4. Memory Stress		HVDD Cold - B/F <b>Pass</b>
5. Memory Stress		LVDD Hot - C/G <b>Pass</b>
6. Memory Stress		LVDD Cold - D/H <b>Pass</b>
6. Power Cycle	Test each corner for 50 cycle in room temp	<b>Pass</b>
Note:		

### Memory Module Image



AVL USE ONLY:							
Completed by:	Andy Chang	Completion Date:	01/26/2015	AVL A#	A10272	AVL W/O	WD2653
Comments:							

Test Results

4C					
Minimum Loading					
Start Date		1/18/2015			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	SV4461	P	P	P	P
CPU1 A2					
CPU1 A3					
CPU1 B1	SV4462	P	P	P	P
CPU1 B2					
CPU1 B3					
CPU1 C1	SV4463	P	P	P	P
CPU1 C2					
CPU1 C3					
CPU1 D1	SV4464	P	P	P	P
CPU1 D2					
CPU1 D3					
CPU2 E1	SV4465	P	P	P	P
CPU2 E2					
CPU2 E3					
CPU2 F1	SV4466	P	P	P	P
CPU2 F2					
CPU2 F3					
CPU2 G1	SV4467	P	P	P	P
CPU2 G2					
CPU2 G3					
CPU2 H1	SV4492	P	P	P	P
CPU2 H2					
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Middle Loading					
Start Date		01/22/15			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	SV4461	P	P	P	P
CPU1 A2	SV4462	P	P	P	P
CPU1 A3					
CPU1 B1	SV4463	P	P	P	P
CPU1 B2	SV4464	P	P	P	P
CPU1 B3					
CPU1 C1	SV4465	P	P	P	P
CPU1 C2	SV4466	P	P	P	P
CPU1 C3					
CPU1 D1	SV4467	P	P	P	P
CPU1 D2	SV4468	P	P	P	P
CPU1 D3					
CPU2 E1	SV4469	P	P	P	P
CPU2 E2	SV4470	P	P	P	P
CPU2 E3					
CPU2 F1	SV4471	P	P	P	P
CPU2 F2	SV4472	P	P	P	P
CPU2 F3					
CPU2 G1	SV4473	P	P	P	P
CPU2 G2	SV4474	P	P	P	P
CPU2 G3					
CPU2 H1	SV4475	P	P	P	P
CPU2 H2	SV4492	P	P	P	P
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Maximum Loading					
Start Date		1/25/2015			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	SV4461	P	P	P	P
CPU1 A2	SV4462	P	P	P	P
CPU1 A3	SV4463	P	P	P	P
CPU1 B1	SV4464	P	P	P	P
CPU1 B2	SV4465	P	P	P	P
CPU1 B3	SV4466	P	P	P	P
CPU1 C1	SV4467	P	P	P	P
CPU1 C2	SV4468	P	P	P	P
CPU1 C3	SV4469	P	P	P	P
CPU1 D1	SV4470	P	P	P	P
CPU1 D2	SV4471	P	P	P	P
CPU1 D3	SV4472	P	P	P	P
CPU2 E1	SV4473	P	P	P	P
CPU2 E2	SV4474	P	P	P	P
CPU2 E3	SV4475	P	P	P	P
CPU2 F1	SV4476	P	P	P	P
CPU2 F2	SV4477	P	P	P	P
CPU2 F3	SV4478	P	P	P	P
CPU2 G1	SV4479	P	P	P	P
CPU2 G2	SV4480	P	P	P	P
CPU2 G3	SV4481	P	P	P	P
CPU2 H1	SV4482	P	P	P	P
CPU2 H2	SV4483	P	P	P	P
CPU2 H3	SV4492	P	P	P	P
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Minimum Loading					
Start Date		1/17/2015			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	SV4461	P	P	P	P
CPU1 A2					
CPU1 A3					
CPU1 B1	SV4462	P	P	P	P
CPU1 B2					
CPU1 B3					
CPU1 C1	SV4463	P	P	P	P
CPU1 C2					
CPU1 C3					
CPU1 D1	SV4464	P	P	P	P
CPU1 D2					
CPU1 D3					
CPU2 E1	SV4465	P	P	P	P
CPU2 E2					
CPU2 E3					
CPU2 F1	SV4466	P	P	P	P
CPU2 F2					
CPU2 F3					
CPU2 G1	SV4467	P	P	P	P
CPU2 G2					
CPU2 G3					
CPU2 H1	SV4468	P	P	P	P
CPU2 H2					
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Middle Loading					
Start Date		01/20/15			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	SV4461	P	P	P	P
CPU1 A2	SV4462	P	P	P	P
CPU1 A3					
CPU1 B1	SV4463	P	P	P	P
CPU1 B2	SV4464	P	P	P	P
CPU1 B3					
CPU1 C1	SV4465	P	P	P	P
CPU1 C2	SV4466	P	P	P	P
CPU1 C3					
CPU1 D1	SV4467	P	P	P	P
CPU1 D2	SV4468	P	P	P	P
CPU1 D3					
CPU2 E1	SV4469	P	P	P	P
CPU2 E2	SV4470	P	P	P	P
CPU2 E3					
CPU2 F1	SV4471	P	P	P	P
CPU2 F2	SV4472	P	P	P	P
CPU2 F3					
CPU2 G1	SV4473	P	P	P	P
CPU2 G2	SV4474	P	P	P	P
CPU2 G3					
CPU2 H1	SV4475	P	P	P	P
CPU2 H2	SV4476	P	P	P	P
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Maximum Loading					
Start Date		1/22/2015			
DIMM Voltage		1.35v			
DIMM	S/N	A	B	C	D
CPU1 A1	SV4461	P	P	P	P
CPU1 A2	SV4462	P	P	P	P
CPU1 A3	SV4463	P	P	P	P
CPU1 B1	SV4464	P	P	P	P
CPU1 B2	SV4465	P	P	P	P
CPU1 B3	SV4466	P	P	P	P
CPU1 C1	SV4467	P	P	P	P
CPU1 C2	SV4468	P	P	P	P
CPU1 C3	SV4469	P	P	P	P
CPU1 D1	SV4470	P	P	P	P
CPU1 D2	SV4471	P	P	P	P
CPU1 D3	SV4472	P	P	P	P
CPU2 E1	SV4473	P	P	P	P
CPU2 E2	SV4474	P	P	P	P
CPU2 E3	SV4475	P	P	P	P
CPU2 F1	SV4476	P	P	P	P
CPU2 F2	SV4477	P	P	P	P
CPU2 F3	SV4478	P	P	P	P
CPU2 G1	SV4479	P	P	P	P
CPU2 G2	SV4480	P	P	P	P
CPU2 G3	SV4481	P	P	P	P
CPU2 H1	SV4482	P	P	P	P
CPU2 H2	SV4483	P	P	P	P
CPU2 H3	SV4484	P	P	P	P
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P