



Advanced Validation Labs, Inc.

17665B Newhope Street, Fountain Valley, CA 92708 (714) 435-2630



Intel PCSD Server Memory Compatibility Test Certificate	
Test System: Intel S2600GZ (Grizzly Pass)	Test Result: Pass

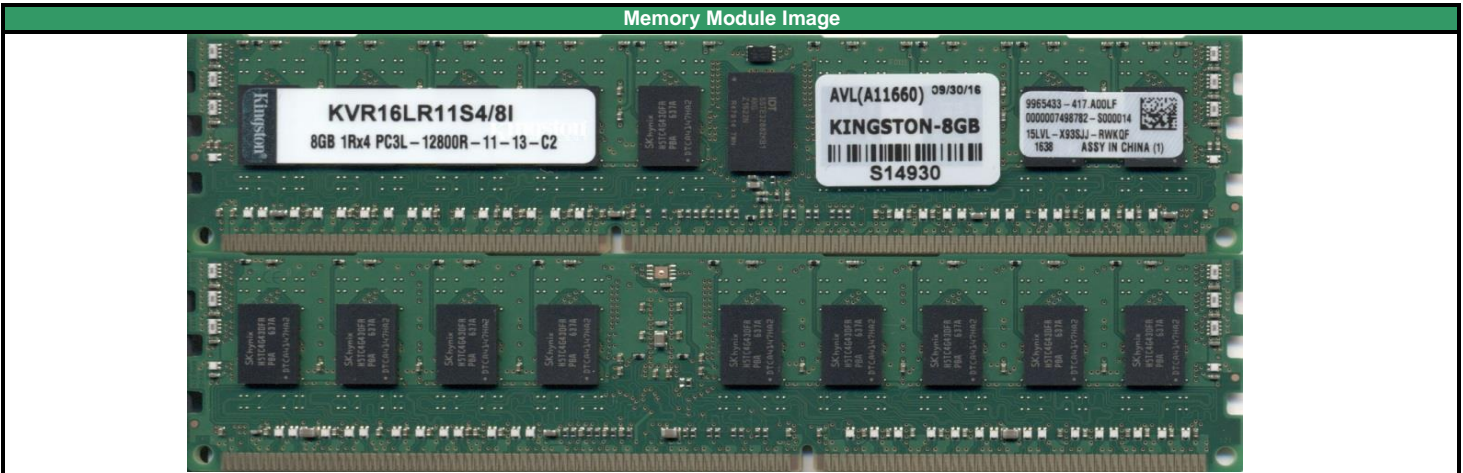
Leveraged System(s):N/A

Module Information									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KVR16LR11S4/8I	RDIMM	1.35V	8GB	1Gx72	1600	11	C	SR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.		DIMM Composition		
SK Hynix	H5TC4G43DFR-PBA	4Gb	1024Mx4bit	1637	IDT	B	(1024Mx4)*72		

Leveraged Memory Modules						
	Vendor	Part Number	Type	Voltage	CL	Speed
1	Kingston	KVR16LR11S4K3/24I	RDIMM	1.35V	11	1600
2	Kingston	KVR16LR11S4K4/32I	RDIMM	1.35V	11	1600
3						
4						
5						
6						

System Configuration		
SETUP	System #1	System #2
AVL S/N	SR2525	SO8251
System S/N	QSGR14500317	QSGR14600736
Board Rev. (PBA)	G11481-301	
CPU Type	E5-2697 v2 / 2.7 GHz	
Chipset	Intel C602	
BIOS / Date	02.06.0005 / 08/03/2016	
BMC / ME	1.27.9958 / 02.01.07.328	
FRU/SDR	1.13	
OS	Windows 2008 Enterprise R2 64bit SP1	
Test Tool	iWVSS 2.6.5, SELViewer, Pmode2, Syscfg, WinPIRA, MemPuller	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD Hot - A/E Pass
4. Memory Stress		HVDD Cold - B/F Pass
5. Memory Stress		LVDD Hot - C/G Pass
6. Memory Stress		LVDD Cold - D/H Pass
6. Power Cycle	Test each corner for 50 cycle in room temp	Pass
Note:		



AVL USE ONLY:							
Completed by:	Andy Chang	Completion Date:	01/12/17	AVL A#	A11660	AVL W/O	WD6977
Comments:							

Test Results

4C						
Minimum Loading						
Start Date		11/1/2016				
DIMM Voltage		1.5v				
DIMM	S/N	A	B	C	D	
CPU1 A1	S14930	P	P	P	P	
CPU1 A2						
CPU1 A3						
CPU1 B1	S14931	P	P	P	P	
CPU1 B2						
CPU1 B3						
CPU1 C1	S14933	P	P	P	P	
CPU1 C2						
CPU1 C3						
CPU1 D1	S14934	P	P	P	P	
CPU1 D2						
CPU1 D3						
CPU2 E1	S14937	P	P	P	P	
CPU2 E2						
CPU2 E3						
CPU2 F1	S14938	P	P	P	P	
CPU2 F2						
CPU2 F3						
CPU2 G1	S14940	P	P	P	P	
CPU2 G2						
CPU2 G3						
CPU2 H1	S14941	P	P	P	P	
CPU2 H2						
CPU2 H3						
AC Power Cycling						
50 AC Cycles/corner						

4C						
Middle Loading						
Start Date		11/20/16				
DIMM Voltage		1.5v				
DIMM	S/N	A	B	C	D	
CPU1 A1	S14930	P	P	P	P	
CPU1 A2	S14931	P	P	P	P	
CPU1 A3						
CPU1 B1	S14933	P	P	P	P	
CPU1 B2	S14934	P	P	P	P	
CPU1 B3						
CPU1 C1	S14937	P	P	P	P	
CPU1 C2	S14938	P	P	P	P	
CPU1 C3						
CPU1 D1	S14940	P	P	P	P	
CPU1 D2	S14941	P	P	P	P	
CPU1 D3						
CPU2 E1	S14942	P	P	P	P	
CPU2 E2	S14943	P	P	P	P	
CPU2 E3						
CPU2 F1	S14945	P	P	P	P	
CPU2 F2	S14946	P	P	P	P	
CPU2 F3						
CPU2 G1	S14949	P	P	P	P	
CPU2 G2	S14950	P	P	P	P	
CPU2 G3						
CPU2 H1	S14952	P	P	P	P	
CPU2 H2	S14953	P	P	P	P	
CPU2 H3						
AC Power Cycling						
50 AC Cycles/corner						

4C						
Maximum Loading						
Start Date		12/1/2016				
DIMM Voltage		1.5v				
DIMM	S/N	A	B	C	D	
CPU1 A1	S14930	P	P	P	P	
CPU1 A2	S14931	P	P	P	P	
CPU1 A3	S14932	P	P	P	P	
CPU1 B1	S14933	P	P	P	P	
CPU1 B2	S14934	P	P	P	P	
CPU1 B3	S14935	P	P	P	P	
CPU1 C1	S14936	P	P	P	P	
CPU1 C2	S14937	P	P	P	P	
CPU1 C3	S14938	P	P	P	P	
CPU1 D1	S14939	P	P	P	P	
CPU1 D2	S14940	P	P	P	P	
CPU1 D3	S14941	P	P	P	P	
CPU2 E1	S14942	P	P	P	P	
CPU2 E2	S14943	P	P	P	P	
CPU2 E3	S14944	P	P	P	P	
CPU2 F1	S14945	P	P	P	P	
CPU2 F2	S14946	P	P	P	P	
CPU2 F3	S14947	P	P	P	P	
CPU2 G1	S14948	P	P	P	P	
CPU2 G2	S14949	P	P	P	P	
CPU2 G3	S14950	P	P	P	P	
CPU2 H1	S14951	P	P	P	P	
CPU2 H2	S14952	P	P	P	P	
CPU2 H3	S14953	P	P	P	P	
AC Power Cycling						
50 AC Cycles/corner						

4C						
Minimum Loading						
Start Date		11/3/2016				
DIMM Voltage		1.35v				
DIMM	S/N	E	F	G	H	
CPU1 A1	S14930	P	P	P	P	
CPU1 A2						
CPU1 A3						
CPU1 B1	S14931	P	P	P	P	
CPU1 B2						
CPU1 B3						
CPU1 C1	S14933	P	P	P	P	
CPU1 C2						
CPU1 C3						
CPU1 D1	S14934	P	P	P	P	
CPU1 D2						
CPU1 D3						
CPU2 E1	S14937	P	P	P	P	
CPU2 E2						
CPU2 E3						
CPU2 F1	S14938	P	P	P	P	
CPU2 F2						
CPU2 F3						
CPU2 G1	S14940	P	P	P	P	
CPU2 G2						
CPU2 G3						
CPU2 H1	S14941	P	P	P	P	
CPU2 H2						
CPU2 H3						
AC Power Cycling						
50 AC Cycles/corner						

4C						
Middle Loading						
Start Date		11/15/16				
DIMM Voltage		1.35v				
DIMM	S/N	E	F	G	H	
CPU1 A1	S14930	P	P	P	P	
CPU1 A2	S14931	P	P	P	P	
CPU1 A3						
CPU1 B1	S14933	P	P	P	P	
CPU1 B2	S14934	P	P	P	P	
CPU1 B3						
CPU1 C1	S14937	P	P	P	P	
CPU1 C2	S14938	P	P	P	P	
CPU1 C3						
CPU1 D1	S14940	P	P	P	P	
CPU1 D2	S14941	P	P	P	P	
CPU1 D3						
CPU2 E1	S14942	P	P	P	P	
CPU2 E2	S14943	P	P	P	P	
CPU2 E3						
CPU2 F1	S14945	P	P	P	P	
CPU2 F2	S14946	P	P	P	P	
CPU2 F3						
CPU2 G1	S14949	P	P	P	P	
CPU2 G2	S14950	P	P	P	P	
CPU2 G3						
CPU2 H1	S14952	P	P	P	P	
CPU2 H2	S14953	P	P	P	P	
CPU2 H3						
AC Power Cycling						
50 AC Cycles/corner						

4C						
Maximum Loading						
Start Date		10/11/2016				
DIMM Voltage		1.35v				
DIMM	S/N	E	F	G	H	
CPU1 A1	S14930	P	P	P	P	
CPU1 A2	S14931	P	P	P	P	
CPU1 A3	S14932	P	P	P	P	
CPU1 B1	S14933	P	P	P	P	
CPU1 B2	S14934	P	P	P	P	
CPU1 B3	S14935	P	P	P	P	
CPU1 C1	S14936	P	P	P	P	
CPU1 C2	S14937	P	P	P	P	
CPU1 C3	S14938	P	P	P	P	
CPU1 D1	S14939	P	P	P	P	
CPU1 D2	S14940	P	P	P	P	
CPU1 D3	S14941	P	P	P	P	
CPU2 E1	S14942	P	P	P	P	
CPU2 E2	S14943	P	P	P	P	
CPU2 E3	S14944	P	P	P	P	
CPU2 F1	S14945	P	P	P	P	
CPU2 F2	S14946	P	P	P	P	
CPU2 F3	S14947	P	P	P	P	
CPU2 G1	S14948	P	P	P	P	
CPU2 G2	S14949	P	P	P	P	
CPU2 G3	S14950	P	P	P	P	
CPU2 H1	S14951	P	P	P	P	
CPU2 H2	S14952	P	P	P	P	
CPU2 H3	S14953	P	P	P	P	
AC Power Cycling						
50 AC Cycles/corner						