



Advanced Validation Labs, Inc.

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Intel PCSD Server Memory Compatibility Test Certificate

Test System: **Intel S2600GZ (Grizzly Pass)**Test Result: **Pass**

Leveraged System(s):N/A

Module Information

DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KVR16LR11D4/16L	RDIMM	1.35V	16GB	2Gx72	1600	11	E	DR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.		DIMM Composition		
Hynix	H5TC4G43DFR-PBA	4Gb	1024Mx4bit	1636	IDT	B	(1024Mx4)*2*72		

Leveraged Memory Modules

	Vendor	Type	Voltage	CL	Speed
1	Kingston	RDIMM	1.35V	11	1600
2	Kingston	RDIMM	1.35V	11	1600
3					
4					
5					
6					

System Configuration

SETUP	System #1	System #2
AVL S/N	SR2525	SO8251
System S/N	QSGR14500317	QSGR14600736
Board Rev. (PBA)	G11481-301	
CPU Type	E5-2697 v2 / 2.7 GHz	
Chipset	Intel C602	
BIOS / Date	02.06.0005 / 08/03/2016	
BMC / ME	1.27.9958 / 02.01.07.328	
FUR/SDR	1.13	
OS	Windows 2008 Enterprise R2 64bit SP1	
Test Tool	iVVSS 2.6.5, SELViewer, Pmode2, Syscfg, WinPIRA, MemPULLer	

Testing Summary

Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD Hot - A/E Pass
4. Memory Stress		HVDD Cold - B/F Pass
5. Memory Stress		LVDD Hot - C/G Pass
6. Memory Stress		LVDD Cold - D/H Pass
6. Power Cycle	Test each corner for 50 cycle in room temp	Pass
Note:		

Memory Module Image



AVL USE ONLY:

Completed by:	Andy Chang	Completion Date:	03/16/2017	AVL A#	A11661	AVL W/O	WD6986
Comments:	* Adjust Voltage : SV-High=1.52v; SV-Low=1.47v; LV-High=1.37v; LV-Low=1.32v .						

Test Results

4C					
Minimum Loading					
Start Date		12/1/2016			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	S15022	P	P	P	P
CPU1 A2					
CPU1 A3					
CPU1 B1	S15023	P	P	P	P
CPU1 B2					
CPU1 B3					
CPU1 C1	S15024	P	P	P	P
CPU1 C2					
CPU1 C3					
CPU1 D1	S15025	P	P	P	P
CPU1 D2					
CPU1 D3					
CPU2 E1	S15026	P	P	P	P
CPU2 E2					
CPU2 E3					
CPU2 F1	S15027	P	P	P	P
CPU2 F2					
CPU2 F3					
CPU2 G1	S15028	P	P	P	P
CPU2 G2					
CPU2 G3					
CPU2 H1	S15029	P	P	P	P
CPU2 H2					
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner					

4C					
Middle Loading					
Start Date		12/1/2016			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	S15005	P	P	P	P
CPU1 A2	S15006	P	P	P	P
CPU1 A3					
CPU1 B1	S15007	P	P	P	P
CPU1 B2	S15008	P	P	P	P
CPU1 B3					
CPU1 C1	S15009	P	P	P	P
CPU1 C2	S15010	P	P	P	P
CPU1 C3					
CPU1 D1	S15011	P	P	P	P
CPU1 D2	S15012	P	P	P	P
CPU1 D3					
CPU2 E1	S14997	P	P	P	P
CPU2 E2	S14998	P	P	P	P
CPU2 E3					
CPU2 F1	S14999	P	P	P	P
CPU2 F2	S15000	P	P	P	P
CPU2 F3					
CPU2 G1	S14988	P	P	P	P
CPU2 G2	S14989	P	P	P	P
CPU2 G3					
CPU2 H1	S14990	P	P	P	P
CPU2 H2	S14991	P	P	P	P
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner					

4C					
Maximum Loading					
Start Date		12/1/2016			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	S15005	P	P	P	P
CPU1 A2	S15006	P	P	P	P
CPU1 A3	S15007	P	P	P	P
CPU1 B1	S15008	P	P	P	P
CPU1 B2	S15009	P	P	P	P
CPU1 B3	S15010	P	P	P	P
CPU1 C1	S15011	P	P	P	P
CPU1 C2	S15012	P	P	P	P
CPU1 C3	S15013	P	P	P	P
CPU1 D1	S15014	P	P	P	P
CPU1 D2	S15015	P	P	P	P
CPU1 D3	S15016	P	P	P	P
CPU2 E1	S15017	P	P	P	P
CPU2 E2	S15018	P	P	P	P
CPU2 E3	S15019	P	P	P	P
CPU2 F1	S15020	P	P	P	P
CPU2 F2	S15021	P	P	P	P
CPU2 F3	S15022	P	P	P	P
CPU2 G1	S15023	P	P	P	P
CPU2 G2	S15024	P	P	P	P
CPU2 G3	S15025	P	P	P	P
CPU2 H1	S15026	P	P	P	P
CPU2 H2	S15027	P	P	P	P
CPU2 H3	S15028	P	P	P	P
AC Power Cycling					
50 AC Cycles/corner					

4C					
Minimum Loading					
Start Date		12/1/2016			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	S15022	P	P	P	P
CPU1 A2					
CPU1 A3					
CPU1 B1	S15023	P	P	P	P
CPU1 B2					
CPU1 B3					
CPU1 C1	S15024	P	P	P	P
CPU1 C2					
CPU1 C3					
CPU1 D1	S15025	P	P	P	P
CPU1 D2					
CPU1 D3					
CPU2 E1	S15026	P	P	P	P
CPU2 E2					
CPU2 E3					
CPU2 F1	S15027	P	P	P	P
CPU2 F2					
CPU2 F3					
CPU2 G1	S15028	P	P	P	P
CPU2 G2					
CPU2 G3					
CPU2 H1	S15029	P	P	P	P
CPU2 H2					
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner					

4C					
Middle Loading					
Start Date		12/1/2016			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	S15005	P	P	P	P
CPU1 A2	S15006	P	P	P	P
CPU1 A3					
CPU1 B1	S15007	P	P	P	P
CPU1 B2	S15008	P	P	P	P
CPU1 B3					
CPU1 C1	S15009	P	P	P	P
CPU1 C2	S15010	P	P	P	P
CPU1 C3					
CPU1 D1	S15011	P	P	P	P
CPU1 D2	S15012	P	P	P	P
CPU1 D3					
CPU2 E1	S14997	P	P	P	P
CPU2 E2	S14998	P	P	P	P
CPU2 E3					
CPU2 F1	S14999	P	P	P	P
CPU2 F2	S15000	P	P	P	P
CPU2 F3					
CPU2 G1	S14988	P	P	P	P
CPU2 G2	S14989	P	P	P	P
CPU2 G3					
CPU2 H1	S14990	P	P	P	P
CPU2 H2	S14991	P	P	P	P
CPU2 H3					
AC Power Cycling					
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4C					
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DIMM	S/N	E	F	G	H
CPU1 A1	S15005	P	P	P	P
CPU1 A2	S15006	P	P	P	P
CPU1 A3	S15007	P	P	P	P
CPU1 B1	S15008	P	P	P	P
CPU1 B2	S15009	P	P	P	P
CPU1 B3	S15010	P	P	P	P
CPU1 C1	S15011	P	P	P	P
CPU1 C2	S15012	P	P	P	P
CPU1 C3	S15013	P	P	P	P
CPU1 D1	S15014	P	P	P	P
CPU1 D2	S15015	P	P	P	P
CPU1 D3	S15016	P	P	P	P
CPU2 E1	S15017	P	P	P	P
CPU2 E2	S15018	P	P	P	P
CPU2 E3	S15019	P	P	P	P
CPU2 F1	S15020	P	P	P	P
CPU2 F2	S15021	P	P	P	P
CPU2 F3	S15022	P	P	P	P
CPU2 G1	S15023	P	P	P	P
CPU2 G2	S15024	P	P	P	P
CPU2 G3	S15025	P	P	P	P
CPU2 H1	S15026	P	P	P	P
CPU2 H2	S15027	P	P	P	P
CPU2 H3	S15028	P	P	P	P
AC Power Cycling					
50 AC Cycles/corner					