Advanced Validation Labs, Inc.

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Intel PCSD Server Memory Compatibility Test Certificate

Test System: **Intel S2600GZ (Grizzly Pass)** Test Result: **Pass**

Leveraged System(s):N/A

Module Information

RPXXXXXXXXXXXXXXXXX Rev. 03/01/2014

DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KVR16LE11/8I	UDIMM ECC	1.35V	8GB	1Gx72	1600	11	E	DR
DRAM Vendor	**DRAM Part Number**	**DRAM Density / Width / Date Code**			**Register Vendor / Rev.**		**DIMM Composition**		
Hynix	H5TC4G83BFR-PBA	4Gb	512Mx8bit	1444			(512Mx8)x2*72		

Leveraged Memory Modules

	Vendor		Type	Voltage	CL	Speed
1	Kingston	KVR16LE11K3/24I	UDIMM ECC	1.35V	11	1600
2	Kingston	KVR16LE11K4/32I	UDIMM ECC	1.35V	11	1600
3	Kingston	KVR16E11/8I	UDIMM ECC	1.5V	11	1600
4	Kingston	KVR16E11K3/24I	UDIMM ECC	1.5V	11	1600
5	Kingston	KVR16E11K4/32I	UDIMM ECC	1.5V	11	1600
6						

System Configuration

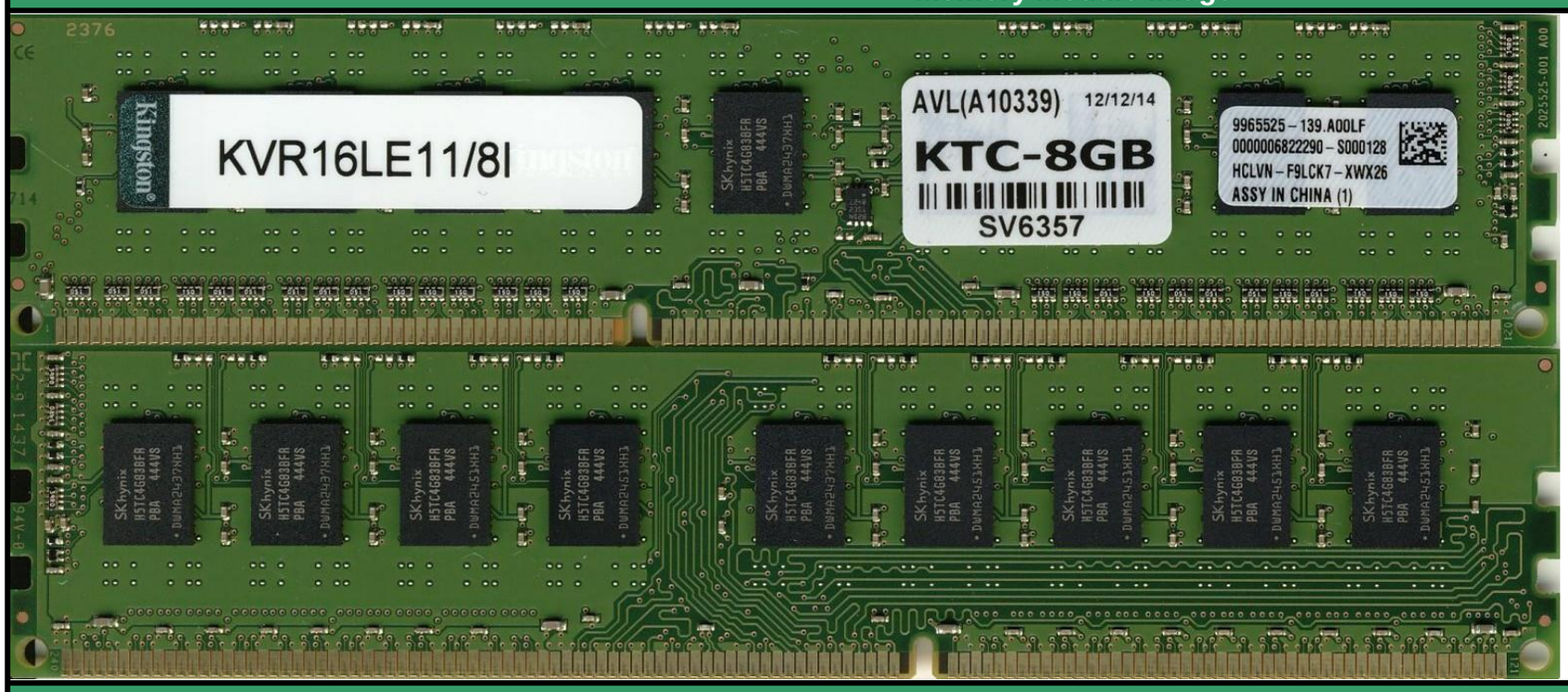
SETUP	System #1	System #2
AVL S/N	SR2525	SO8251
System S/N	QSGR14500317	QSGR14600736
Board Rev. (PBA)	G11481-301	
CPU Type	E5-2697 v2 / 2.7 GHz	
Chipset	Intel C602	
BIOS / Date	02.03.2003 / 06/19/2014	
BMC / ME	1.19.5018 / 02.01.07.231	
FUR/SDR	1.13	
OS	Windows 2008 Enterprise R2 64bit SP1	
Test Tool	iWVSS 2.5.3, SELViewer, Pvmode2, Syscfg, WinPIRA,MemPuller	

Testing Summary

Test Items	Test Description		Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS		Done
2. SPD Check	DIMM SPD content check for JEDEC compliance		Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD Hot - A/E	Pass
4. Memory Stress		HVDD Cold - B/F	Pass
5. Memory Stress		LVDD Hot - C/G	Pass
6. Memory Stress		LVDD Cold - D/H	Pass
6. Power Cycle	Test each corner for 50 cycle in room temp		Pass

Note:

Memory Module Image



AVL USE ONLY:

Completed by:	Andy Chang	Completion Date:	02/03/15	AVL A#	A10339		AVL W/O	WD2921

Comments:

Test Results

4C Minimum Loading					
Start Date		1/23/2015			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	SV6357	P	P	P	P
CPU1 A2					
CPU1 A3					
CPU1 B1	SV6358	P	P	P	P
CPU1 B2					
CPU1 B3					
CPU1 C1	SV6359	P	P	P	P
CPU1 C2					
CPU1 C3					
CPU1 D1	SV6360	P	P	P	P
CPU1 D2					
CPU1 D3					
CPU2 E1	SV6361	P	P	P	P
CPU2 E2					
CPU2 E3					
CPU2 F1	SV6362	P	P	P	P
CPU2 F2					
CPU2 F3					
CPU2 G1	SV6363	P	P	P	P
CPU2 G2					
CPU2 G3					
CPU2 H1	SV6260	P	P	P	P
CPU2 H2					
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C Middle Loading					
Start Date		01/29/15			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	SV6357	P	P	P	P
CPU1 A2	SV6358	P	P	P	P
CPU1 A3					
CPU1 B1	SV6359	P	P	P	P
CPU1 B2	SV6360	P	P	P	P
CPU1 B3					
CPU1 C1	SV6361	P	P	P	P
CPU1 C2	SV6362	P	P	P	P
CPU1 C3					
CPU1 D1	SV6363	P	P	P	P
CPU1 D2	SV6364	P	P	P	P
CPU1 D3					
CPU2 E1	SV6365	P	P	P	P
CPU2 E2	SV6366	P	P	P	P
CPU2 E3					
CPU2 F1	SV6367	P	P	P	P
CPU2 F2	SV6368	P	P	P	P
CPU2 F3					
CPU2 G1	SV6369	P	P	P	P
CPU2 G2	SV6370	P	P	P	P
CPU2 G3					
CPU2 H1	SV6371	P	P	P	P
CPU2 H2	SV6260	P	P	P	P
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C Maximum Loading					
Start Date					
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1					
CPU1 A2					
CPU1 A3					
CPU1 B1					
CPU1 B2					
CPU1 B3					
CPU1 C1					
CPU1 C2					
CPU1 C3					
CPU1 D1					
CPU1 D2					
CPU1 D3					
CPU2 E1					
CPU2 E2					
CPU2 E3					
CPU2 F1					
CPU2 F2					
CPU2 F3					
CPU2 G1					
CPU2 G2					
CPU2 G3					
CPU2 H1					
CPU2 H2					
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner					

4C Minimum Loading					
Start Date		1/26/2015			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	SV6357	P	P	P	P
CPU1 A2					
CPU1 A3					
CPU1 B1	SV6358	P	P	P	P
CPU1 B2					
CPU1 B3					
CPU1 C1	SV6359	P	P	P	P
CPU1 C2					
CPU1 C3					
CPU1 D1	SV6360	P	P	P	P
CPU1 D2					
CPU1 D3					
CPU2 E1	SV6361	P	P	P	P
CPU2 E2					
CPU2 E3					
CPU2 F1	SV6362	P	P	P	P
CPU2 F2					
CPU2 F3					
CPU2 G1	SV6363	P	P	P	P
CPU2 G2					
CPU2 G3					
CPU2 H1	SV6364	P	P	P	P
CPU2 H2					
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C Middle Loading					
Start Date		01/31/15			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	SV6357	P	P	P	P
CPU1 A2	SV6358	P	P	P	P
CPU1 A3					
CPU1 B1	SV6359	P	P	P	P
CPU1 B2	SV6360	P	P	P	P
CPU1 B3					
CPU1 C1	SV6361	P	P	P	P
CPU1 C2	SV6362	P	P	P	P
CPU1 C3					
CPU1 D1	SV6363	P	P	P	P
CPU1 D2	SV6364	P	P	P	P
CPU1 D3					
CPU2 E1	SV6365	P	P	P	P
CPU2 E2	SV6366	P	P	P	P
CPU2 E3					
CPU2 F1	SV6367	P	P	P	P
CPU2 F2	SV6368	P	P	P	P
CPU2 F3					
CPU2 G1	SV6369	P	P	P	P
CPU2 G2	SV6370	P	P	P	P
CPU2 G3					
CPU2 H1	SV6371	P	P	P	P
CPU2 H2	SV6372	P	P	P	P
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C Maximum Loading					
Start Date					
DIMM Voltage		1.35v			
DIMM	S/N	A	B	C	D
CPU1 A1					
CPU1 A2					
CPU1 A3					
CPU1 B1					
CPU1 B2					
CPU1 B3					
CPU1 C1					
CPU1 C2					
CPU1 C3					
CPU1 D1					
CPU1 D2					
CPU1 D3					
CPU2 E1					
CPU2 E2					
CPU2 E3					
CPU2 F1					
CPU2 F2					
CPU2 F3					
CPU2 G1					
CPU2 G2					
CPU2 G3					
CPU2 H1					
CPU2 H2					
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner					