



## Advanced Validation Labs, Inc.

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### Intel PCSD Server Memory Compatibility Test Certificate

Test System: **Intel S2600GZ (Grizzly Pass)**

Test Result: **Pass**



Leveraged System(s):N/A

#### Module Information

DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KVR16LE11S8/4I	UDIMM ECC	1.35V	4GB	512Mx72	1600	11	D	SR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.		DIMM Composition		
Hynix	H5TC4G83DFR-PBA	4Gb	512Mx8bit	1637			(512Mx8)*72		

#### Leveraged Memory Modules

Vendor	Type	Voltage	CL	Speed
1				
2				
3				
4				
5				
6				

#### System Configuration

SETUP	System #1	System #2
AVL S/N	SR2525	SO8251
System S/N	QSGR14500317	QSGR14600736
Board Rev. (PBA)	G11481-301	
CPU Type	E5-2697 v2 / 2.7 GHz	
Chipset	Intel C602	
BIOS / Date	02.06.0005 / 08/03/2016	
BMC / ME	1.27.9958 / 02.01.07.328	
FUR/SDR	1.13	
OS	Windows 2008 Enterprise R2 64bit SP1	
Test Tool	iWVSS 2.6.5, SELViewer, Pmode2, Syscfg, WinPIRA, MemPuller	

#### Testing Summary

Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD Hot - A/E Pass
4. Memory Stress		HVDD Cold - B/F Pass
5. Memory Stress		LVDD Hot - C/G Pass
6. Memory Stress		LVDD Cold - D/H Pass
6. Power Cycle	Test each corner for 50 cycle in room temp	Pass
Note:		

#### Memory Module Image



#### AVL USE ONLY:

Completed by:	Andy Chang	Completion Date:	02/24/2017	AVL A#	A11662	AVL W/O	WD6987
Comments:	* Adjust Voltage on MaxLoad Testing. SV-High=1.52v; SV-Low=1.47v; LV-High=1.37v; LV-Low=1.32v .						

Test Results

4C					
Minimum Loading					
Start Date		11/14/2016			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	S15032	P	P	P	P
CPU1 A2					
CPU1 A3					
CPU1 B1	S15033	P	P	P	P
CPU1 B2					
CPU1 B3					
CPU1 C1	S15034	P	P	P	P
CPU1 C2					
CPU1 C3					
CPU1 D1	S15035	P	P	P	P
CPU1 D2					
CPU1 D3					
CPU2 E1	S15036	P	P	P	P
CPU2 E2					
CPU2 E3					
CPU2 F1	S15037	P	P	P	P
CPU2 F2					
CPU2 F3					
CPU2 G1	S15038	P	P	P	P
CPU2 G2					
CPU2 G3					
CPU2 H1	S15039	P	P	P	P
CPU2 H2					
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner					

4C					
Middle Loading					
Start Date		11/21/16			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	S16886	P	P	P	P
CPU1 A2	S16887	P	P	P	P
CPU1 A3					
CPU1 B1	S16888	P	P	P	P
CPU1 B2	S16889	P	P	P	P
CPU1 B3					
CPU1 C1	S16890	P	P	P	P
CPU1 C2	S16891	P	P	P	P
CPU1 C3					
CPU1 D1	S16892	P	P	P	P
CPU1 D2	S16893	P	P	P	P
CPU1 D3					
CPU2 E1	S16894	P	P	P	P
CPU2 E2	S16895	P	P	P	P
CPU2 E3					
CPU2 F1	S16896	P	P	P	P
CPU2 F2	S16897	P	P	P	P
CPU2 F3					
CPU2 G1	S16898	P	P	P	P
CPU2 G2	S16899	P	P	P	P
CPU2 G3					
CPU2 H1	S16900	P	P	P	P
CPU2 H2	S16901	P	P	P	P
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner					

4C					
Maximum Loading					
Start Date					
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1					
CPU1 A2					
CPU1 A3					
CPU1 B1					
CPU1 B2					
CPU1 B3					
CPU1 C1					
CPU1 C2					
CPU1 C3					
CPU1 D1					
CPU1 D2					
CPU1 D3					
CPU2 E1					
CPU2 E2					
CPU2 E3					
CPU2 F1					
CPU2 F2					
CPU2 F3					
CPU2 G1					
CPU2 G2					
CPU2 G3					
CPU2 H1					
CPU2 H2					
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner					

4C					
Minimum Loading					
Start Date		11/14/2016			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	S15032	P	P	P	P
CPU1 A2					
CPU1 A3					
CPU1 B1	S15033	P	P	P	P
CPU1 B2					
CPU1 B3					
CPU1 C1	S15034	P	P	P	P
CPU1 C2					
CPU1 C3					
CPU1 D1	S15035	P	P	P	P
CPU1 D2					
CPU1 D3					
CPU2 E1	S15036	P	P	P	P
CPU2 E2					
CPU2 E3					
CPU2 F1	S15037	P	P	P	P
CPU2 F2					
CPU2 F3					
CPU2 G1	S15038	P	P	P	P
CPU2 G2					
CPU2 G3					
CPU2 H1	S15039	P	P	P	P
CPU2 H2					
CPU2 H3					
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50 AC Cycles/corner					

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CPU1 A1	S16886	P	P	P	P
CPU1 A2	S16887	P	P	P	P
CPU1 A3					
CPU1 B1	S16888	P	P	P	P
CPU1 B2	S16889	P	P	P	P
CPU1 B3					
CPU1 C1	S16890	P	P	P	P
CPU1 C2	S16891	P	P	P	P
CPU1 C3					
CPU1 D1	S16892	P	P	P	P
CPU1 D2	S16893	P	P	P	P
CPU1 D3					
CPU2 E1	S16894	P	P	P	P
CPU2 E2	S16895	P	P	P	P
CPU2 E3					
CPU2 F1	S16896	P	P	P	P
CPU2 F2	S16897	P	P	P	P
CPU2 F3					
CPU2 G1	S16898	P	P	P	P
CPU2 G2	S16899	P	P	P	P
CPU2 G3					
CPU2 H1	S16900	P	P	P	P
CPU2 H2	S16901	P	P	P	P
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner					

4C					
Maximum Loading					
Start Date					
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1					
CPU1 A2					
CPU1 A3					
CPU1 B1					
CPU1 B2					
CPU1 B3					
CPU1 C1					
CPU1 C2					
CPU1 C3					
CPU1 D1					
CPU1 D2					
CPU1 D3					
CPU2 E1					
CPU2 E2					
CPU2 E3					
CPU2 F1					
CPU2 F2					
CPU2 F3					
CPU2 G1					
CPU2 G2					
CPU2 G3					
CPU2 H1					
CPU2 H2					
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner					