



Advanced Validation Labs, Inc.

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Intel PCSD Server Memory Compatibility Test Certificate

Test System: **Intel S2600GZ (Grizzly Pass)**

Test Result: **Pass**



Leveraged System(s):N/A

Module Information

DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KVR16LE11/8I	UDIMM ECC	1.35V	8GB	1Gx72	1600	11	E	DR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.		DIMM Composition		
Hynix	H5TC4G83DFR-PBA	4Gb	512Mx8bit	1637			(512Mx8)*2*72		

Leveraged Memory Modules

	Vendor	Type	Voltage	CL	Speed
1	Kingston	UDIMM ECC	1.35V	11	1600
2	Kingston	UDIMM ECC	1.35V	11	1600
3					
4					
5					
6					

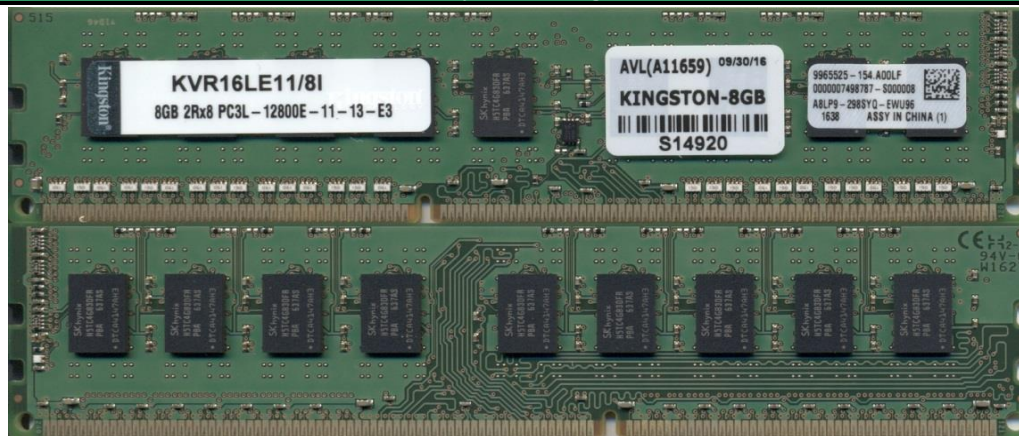
System Configuration

SETUP	System #1	System #2
AVL S/N	SR2525	SO8251
System S/N	QSGR14500317	QSGR14600736
Board Rev. (PBA)	G11481-301	
CPU Type	E5-2697 v2 / 2.7 GHz	
Chipset	Intel C602	
BIOS / Date	02.06.0005 / 08/03/2016	
BMC / ME	1.27.9958 / 02.01.07.328	
FUR/SDR	1.13	
OS	Windows 2008 Enterprise R2 64bit SP1	
Test Tool	iWVSS 2.6.5, SELViewer, Pmode2, Syscfg, WinPIRA, MemPuller	

Testing Summary

Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD Hot - A/E Pass
4. Memory Stress		HVDD Cold - B/F Pass
5. Memory Stress		LVDD Hot - C/G Pass
6. Memory Stress		LVDD Cold - D/H Pass
6. Power Cycle	Test each corner for 50 cycle in room temp	Pass
Note:		

Memory Module Image



AVL USE ONLY:

Completed by:	Andy Chang	Completion Date:	02/10/2017	AVL A#	A11659	AVL W/O	WD6967
Comments:							

Test Results

4C					
Minimum Loading					
Start Date		1/9/2017			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	S14920	P	P	P	P
CPU1 A2					
CPU1 A3					
CPU1 B1	S14921	P	P	P	P
CPU1 B2					
CPU1 B3					
CPU1 C1	S14922	P	P	P	P
CPU1 C2					
CPU1 C3					
CPU1 D1	S14923	P	P	P	P
CPU1 D2					
CPU1 D3					
CPU2 E1	S14925	P	P	P	P
CPU2 E2					
CPU2 E3					
CPU2 F1	S14926	P	P	P	P
CPU2 F2					
CPU2 F3					
CPU2 G1	S14927	P	P	P	P
CPU2 G2					
CPU2 G3					
CPU2 H1	S14928	P	P	P	P
CPU2 H2					
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner					

4C					
Middle Loading					
Start Date		1/9/2017			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	S14920	P	P	P	P
CPU1 A2	S14921	P	P	P	P
CPU1 A3					
CPU1 B1	S14922	P	P	P	P
CPU1 B2	S14923	P	P	P	P
CPU1 B3					
CPU1 C1	S14925	P	P	P	P
CPU1 C2	S14926	P	P	P	P
CPU1 C3					
CPU1 D1	S14927	P	P	P	P
CPU1 D2	S14928	P	P	P	P
CPU1 D3					
CPU2 E1	S16876	P	P	P	P
CPU2 E2	S16877	P	P	P	P
CPU2 E3					
CPU2 F1	S16878	P	P	P	P
CPU2 F2	S16879	P	P	P	P
CPU2 F3					
CPU2 G1	S16880	P	P	P	P
CPU2 G2	S16881	P	P	P	P
CPU2 G3					
CPU2 H1	S16882	P	P	P	P
CPU2 H2	S16883	P	P	P	P
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner					

4C					
Maximum Loading					
Start Date					
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1					
CPU1 A2					
CPU1 A3					
CPU1 B1					
CPU1 B2					
CPU1 B3					
CPU1 C1					
CPU1 C2					
CPU1 C3					
CPU1 D1					
CPU1 D2					
CPU1 D3					
CPU2 E1					
CPU2 E2					
CPU2 E3					
CPU2 F1					
CPU2 F2					
CPU2 F3					
CPU2 G1					
CPU2 G2					
CPU2 G3					
CPU2 H1					
CPU2 H2					
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner					

4C					
Minimum Loading					
Start Date		1/9/2017			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	S14920	P	P	P	P
CPU1 A2					
CPU1 A3					
CPU1 B1	S14921	P	P	P	P
CPU1 B2					
CPU1 B3					
CPU1 C1	S14922	P	P	P	P
CPU1 C2					
CPU1 C3					
CPU1 D1	S14923	P	P	P	P
CPU1 D2					
CPU1 D3					
CPU2 E1	S14925	P	P	P	P
CPU2 E2					
CPU2 E3					
CPU2 F1	S14926	P	P	P	P
CPU2 F2					
CPU2 F3					
CPU2 G1	S14927	P	P	P	P
CPU2 G2					
CPU2 G3					
CPU2 H1	S14928	P	P	P	P
CPU2 H2					
CPU2 H3					
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50 AC Cycles/corner					

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Middle Loading					
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CPU1 A1	S14920	P	P	P	P
CPU1 A2	S14921	P	P	P	P
CPU1 A3					
CPU1 B1	S14922	P	P	P	P
CPU1 B2	S14923	P	P	P	P
CPU1 B3					
CPU1 C1	S14925	P	P	P	P
CPU1 C2	S14926	P	P	P	P
CPU1 C3					
CPU1 D1	S14927	P	P	P	P
CPU1 D2	S14928	P	P	P	P
CPU1 D3					
CPU2 E1	S16876	P	P	P	P
CPU2 E2	S16877	P	P	P	P
CPU2 E3					
CPU2 F1	S16878	P	P	P	P
CPU2 F2	S16879	P	P	P	P
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CPU2 G1	S16880	P	P	P	P
CPU2 G2	S16881	P	P	P	P
CPU2 G3					
CPU2 H1	S16882	P	P	P	P
CPU2 H2	S16883	P	P	P	P
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner					

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Maximum Loading					
Start Date					
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1					
CPU1 A2					
CPU1 A3					
CPU1 B1					
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CPU1 C2					
CPU1 C3					
CPU1 D1					
CPU1 D2					
CPU1 D3					
CPU2 E1					
CPU2 E2					
CPU2 E3					
CPU2 F1					
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AC Power Cycling					
50 AC Cycles/corner					