Advanced Validation Labs, Inc.

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Intel PCSD Server Memory Compatibility Test Certificate

Test System: **Intel S2600CR (Crown Pass)** Test Result: **Pass**

Leveraged System(s): Intel S2600IP (Iron Pass), Intel S2600CO (Copper Pass)

Module Information

DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KVR16LR11D8/8I	RDIMM	1.35V	8GB	1Gx72	1600	11	B	DR

DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.		DIMM Composition
Hynix	HTC4G83BFR-PBA	4Gb	512Mx8bit	1452	IDT	A	(512Mx8)x2*72

Leveraged Memory Modules

	Vendor		Type	Voltage	CL	Speed
1	Kingston	KVR16LR11D8K3/24I	RDIMM	1.35V	11	1600
2	Kingston	KVR16LR11D8K4/32I	RDIMM	1.35V	11	1600
3	Kingston	KVR16R11D8/8I	RDIMM	1.5V	11	1600
4	Kingston	KVR16R11D8K3/24I	RDIMM	1.5V	11	1600
5	Kingston	KVR16R11D8K4/32I	RDIMM	1.5V	11	1600
6						

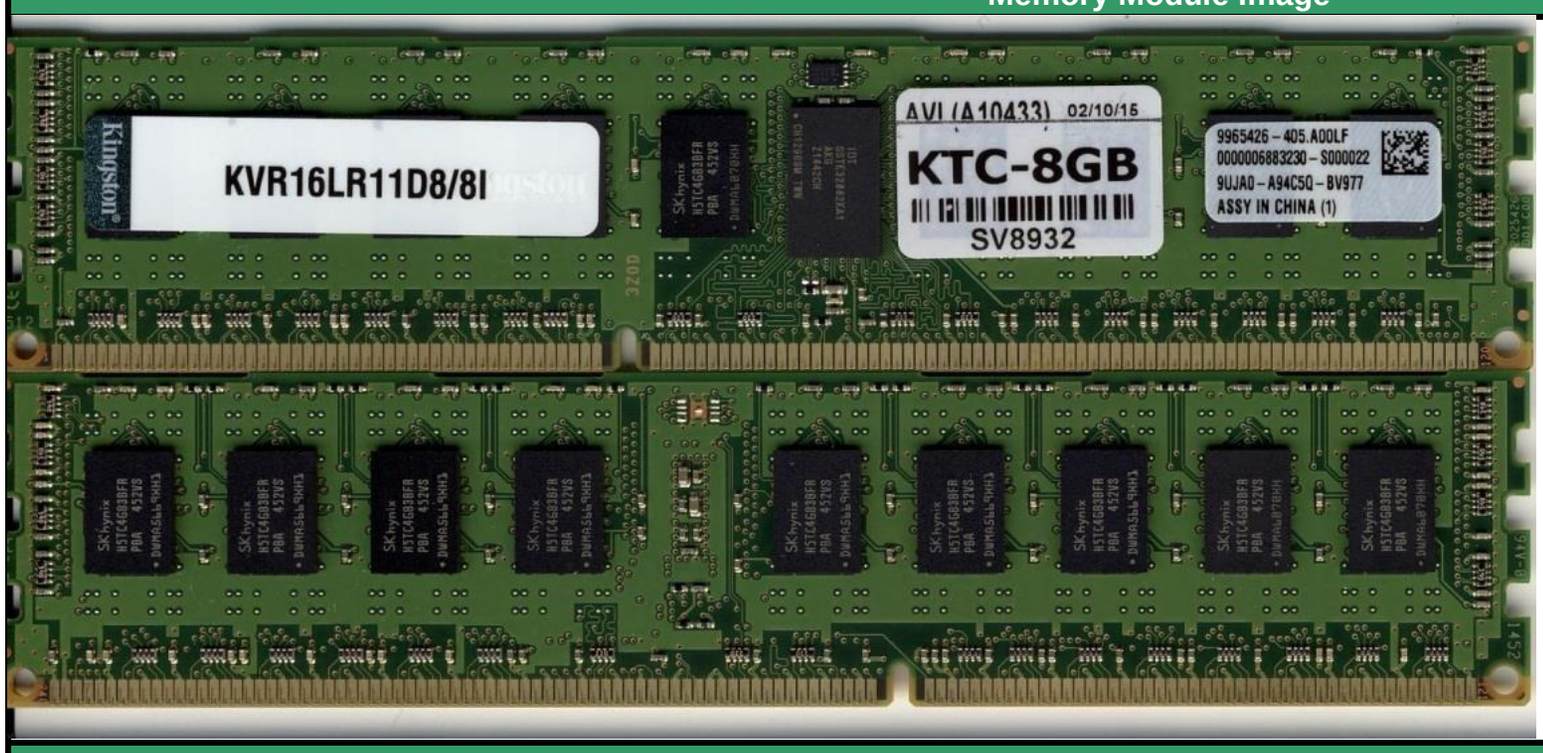
System Configuration

SETUP	System #1	System #2
AVL S/N	SO9588	SU4442
System S/N	QSIP20200687	QSIP22205522
Board Rev. (PBA)	G21602-301	G20602-302
CPU Type	E5-2637 v2 / 3.5 GHz	
Chipset	Intel C602	
BIOS / Date	02.03.0003 / 04/19/2014	
BMC / ME	01.21.6038 / 02.01.07.328	
FUR/SDR	1.09	
OS	Windows 2008 Enterprise R2 64bit SP1	
Test Tool	iWVSS 2.5.3, SELViewer, Pvmode2, Syscfg, WinPIRA,MemPuller	

Testing Summary

Test Items	Test Description		Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS		Done
2. SPD Check	DIMM SPD content check for JEDEC compliance		Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD Hot	Pass
4. Memory Stress		HVDD Cold	Pass
5. Memory Stress		LVDD Hot	Pass
6. Memory Stress		LVDD Cold	Pass
Note:			

Memory Module Image



AVL USE ONLY:

Completed by:	Andy Chang	Completion Date:	03/27/15	AVL A#	A10433		AVL W/O	WD3193

Comments:

Test Results

4C Minimum Loading					
Start Date		3/9/2015			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	SV9007	P	P	P	P
CPU1 A2					
CPU1 B1	SV9008	P	P	P	P
CPU1 B2					
CPU1 C1	SV9009	P	P	P	P
CPU1 C2					
CPU1 D1	SV9010	P	P	P	P
CPU1 D2					
CPU2 E1	SV9011	P	P	P	P
CPU2 E2					
CPU2 F1	SV9012	P	P	P	P
CPU2 F2					
CPU2 G1	SV9113	P	P	P	P
CPU2 G2					
CPU2 H1	SV9030	P	P	P	P
CPU2 H2					

4C Maximum Loading					
Start Date		03/05/15			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	SV9007	P	P	P	P
CPU1 A2	SV9008	P	P	P	P
CPU1 B1	SV9009	P	P	P	P
CPU1 B2	SV9010	P	P	P	P
CPU1 C1	SV9011	P	P	P	P
CPU1 C2	SV9012	P	P	P	P
CPU1 D1	SV9013	P	P	P	P
CPU1 D2	SV9014	P	P	P	P
CPU2 E1	SV9015	P	P	P	P
CPU2 E2	SV9016	P	P	P	P
CPU2 F1	SV9017	P	P	P	P
CPU2 F2	SV9018	P	P	P	P
CPU2 G1	SV9019	P	P	P	P
CPU2 G2	SV9020	P	P	P	P
CPU2 H1	SV9021	P	P	P	P
CPU2 H2	SV9030	P	P	P	P

4C Minimum Loading					
Start Date		3/11/2015			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	SV9007	P	P	P	P
CPU1 A2					
CPU1 B1	SV9008	P	P	P	P
CPU1 B2					
CPU1 C1	SV9009	P	P	P	P
CPU1 C2					
CPU1 D1	SV9010	P	P	P	P
CPU1 D2					
CPU2 E1	SV9011	P	P	P	P
CPU2 E2					
CPU2 F1	SV9012	P	P	P	P
CPU2 F2					
CPU2 G1	SV9113	P	P	P	P
CPU2 G2					
CPU2 H1	SV9114	P	P	P	P
CPU2 H2					

4C Maximum Loading					
Start Date		03/03/15			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	SV9007	P	P	P	P
CPU1 A2	SV9008	P	P	P	P
CPU1 B1	SV9009	P	P	P	P
CPU1 B2	SV9010	P	P	P	P
CPU1 C1	SV9011	P	P	P	P
CPU1 C2	SV9012	P	P	P	P
CPU1 D1	SV9013	P	P	P	P
CPU1 D2	SV9014	P	P	P	P
CPU2 E1	SV9015	P	P	P	P
CPU2 E2	SV9016	P	P	P	P
CPU2 F1	SV9017	P	P	P	P
CPU2 F2	SV9018	P	P	P	P
CPU2 G1	SV9019	P	P	P	P
CPU2 G2	SV9020	P	P	P	P
CPU2 H1	SV9021	P	P	P	P
CPU2 H2	SV9022	P	P	P	P