



Intel PCSD Server Memory Compatibility Test Certificate

Test System: Intel S2600CR (Crown Pass)	Test Result: Pass
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Leveraged System(s): Intel S2600IP (Iron Pass), Intel S2600CO (Copper Pass)

Module Information									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KVR16LE11/8I	UDIMM ECC	1.35V	8GB	1Gx72	1600	11	E	DR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.		DIMM Composition		
Hynix	H5TC4G83BFR-PBA	4Gb	512Mx8bit	1444			(512Mx8)x2*72		

System Configuration		
SETUP	System #1	System #2
AVL S/N	SO9588	SU4442
System S/N	QSIP20200687	QSIP22205522
Board Rev. (PBA)	G21602-301	G20602-302
CPU Type	E5-2637 v2 / 3.5 GHz	
Chipset	Intel C602	
BIOS / Date	02.03.0003 / 04/19/2014	
BMC / ME	01.21.6038 / 02.01.07.328	
FUR/SDR	1.09	
OS	Windows 2008 Enterprise R2 64bit SP1	
Test Tool	iWVSS 2.5.3, SELViewer, Pvmode2, Syscfg, WinPIRA, MemPuller	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD Hot Pass
4. Memory Stress		HVDD Cold Pass
5. Memory Stress		LVDD Hot Pass
6. Memory Stress		LVDD Cold Pass
Note:		

Memory Module Image

AVL USE ONLY:

Completed by:	Andy Chang	Completion Date:	01/30/2015	AVL A#	A10339	AVL W/O	WD2926
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Comments:

Test Results

4C						
Minimum Loading						
Start Date		1/28/2015				
DIMM Voltage		1.5v				
DIMM	S/N	A	B	C	D	
CPU1 A1	SV6391	P	P	P	P	
CPU1 A2						
CPU1 B1	SV6392	P	P	P	P	
CPU1 B2						
CPU1 C1	SV6393	P	P	P	P	
CPU1 C2						
CPU1 D1	SV6394	P	P	P	P	
CPU1 D2						
CPU2 E1	SV6395	P	P	P	P	
CPU2 E2						
CPU2 F1	SV6396	P	P	P	P	
CPU2 F2						
CPU2 G1	SV6397	P	P	P	P	
CPU2 G2						
CPU2 H1	SV6260	P	P	P	P	
CPU2 H2						

4C						
Middle Loading						
Start Date		01/25/15				
DIMM Voltage		1.5v				
DIMM	S/N	A	B	C	D	
CPU1 A1	SV6391	P	P	P	P	
CPU1 A2	SV6392	P	P	P	P	
CPU1 B1	SV6393	P	P	P	P	
CPU1 B2	SV6394	P	P	P	P	
CPU1 C1	SV6395	P	P	P	P	
CPU1 C2	SV6396	P	P	P	P	
CPU1 D1	SV6397	P	P	P	P	
CPU1 D2	SV6398	P	P	P	P	
CPU2 E1	SV6399	P	P	P	P	
CPU2 E2	SV6400	P	P	P	P	
CPU2 F1	SV6401	P	P	P	P	
CPU2 F2	SV6402	P	P	P	P	
CPU2 G1	SV6403	P	P	P	P	
CPU2 G2	SV6404	P	P	P	P	
CPU2 H1	SV6405	P	P	P	P	
CPU2 H2	SV6258	P	P	P	P	



4C						
Minimum Loading						
Start Date		1/26/2015				
DIMM Voltage		1.35v				
DIMM	S/N	E	F	G	H	
CPU1 A1	SV6391	P	P	P	P	
CPU1 A2						
CPU1 B1	SV6392	P	P	P	P	
CPU1 B2						
CPU1 C1	SV6393	P	P	P	P	
CPU1 C2						
CPU1 D1	SV6394	P	P	P	P	
CPU1 D2						
CPU2 E1	SV6395	P	P	P	P	
CPU2 E2						
CPU2 F1	SV6396	P	P	P	P	
CPU2 F2						
CPU2 G1	SV6397	P	P	P	P	
CPU2 G2						
CPU2 H1	SV6398	P	P	P	P	
CPU2 H2						

4C						
Middle Loading						
Start Date		01/22/15				
DIMM Voltage		1.35v				
DIMM	S/N	E	F	G	H	
CPU1 A1	SV6391	P	P	P	P	
CPU1 A2	SV6392	P	P	P	P	
CPU1 B1	SV6393	P	P	P	P	
CPU1 B2	SV6394	P	P	P	P	
CPU1 C1	SV6395	P	P	P	P	
CPU1 C2	SV6396	P	P	P	P	
CPU1 D1	SV6397	P	P	P	P	
CPU1 D2	SV6398	P	P	P	P	
CPU2 E1	SV6399	P	P	P	P	
CPU2 E2	SV6400	P	P	P	P	
CPU2 F1	SV6401	P	P	P	P	
CPU2 F2	SV6402	P	P	P	P	
CPU2 G1	SV6403	P	P	P	P	
CPU2 G2	SV6404	P	P	P	P	
CPU2 H1	SV6405	P	P	P	P	
CPU2 H2	SV6406	P	P	P	P	

