



**Advanced Validation Labs, Inc.**  
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Intel PCSD Server Memory Compatibility Test Certificate	
Test System: <b>Intel S2600CP (Canoe Pass)</b>	Test Result: <b>Pass</b>

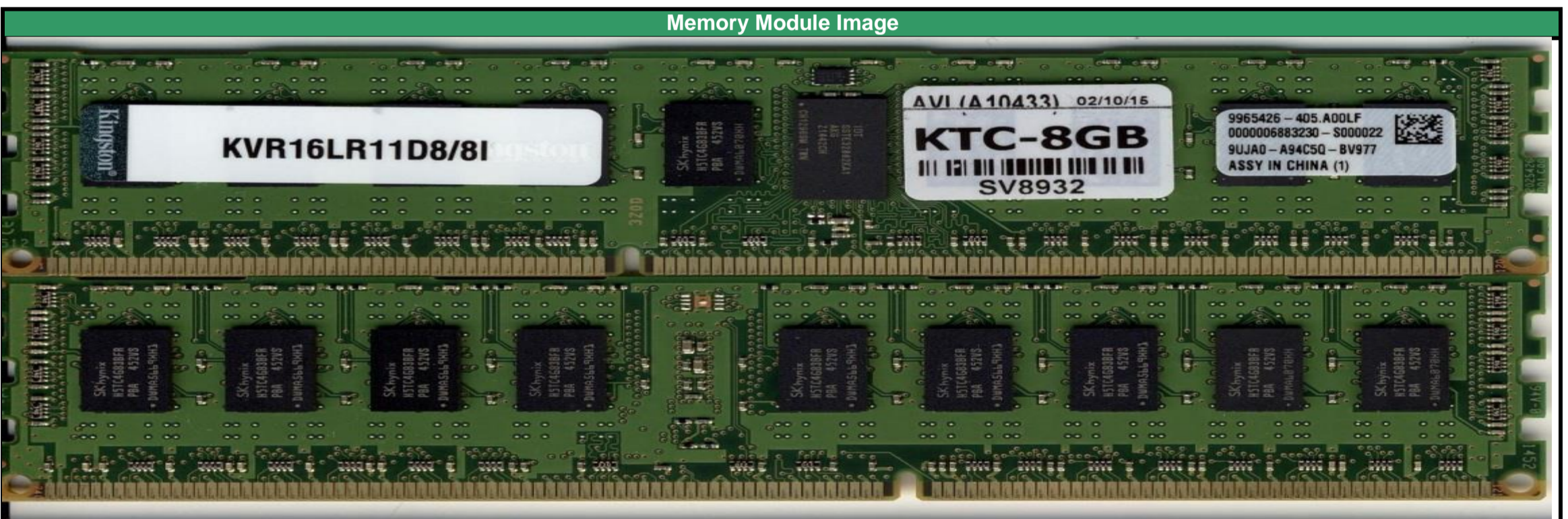
Leveraged System(s): N/A

Module Information									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KVR16LR11D8/8I	RDIMM	1.35V	8GB	1Gx72	1600	11	B	DR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.		DIMM Composition		
Hynix	HTC4G83BFR-PBA	4Gb	512Mx8bit	1452	IDT	A	(512Mx8)x2*72		

Leveraged Memory Modules						
	Vendor	Type	Voltage	CL	Speed	
1	Kingston	KVR16LR11D8K3/24I	RDIMM	1.35V	11	1600
2	Kingston	KVR16LR11D8K4/32I	RDIMM	1.35V	11	1600
3	Kingston	KVR16R11D8/8I	RDIMM	1.5V	11	1600
4	Kingston	KVR16R11D8K3/24I	RDIMM	1.5V	11	1600
5	Kingston	KVR16R11D8K4/32I	RDIMM	1.5V	11	1600
6						

System Configuration		
SETUP	System #1	System #2
AVL S/N	SN2311	SN6442
System S/N	QSCP13800398	QSCP14800221
Board Rev. (PBA)	E99552-401	G50768-501
CPU Type	E5-2680 v2 / 2.8 GHz	
Chipset	Intel C602	
BIOS / Date	02.03.0003 / 04/19/2014	
BMC / ME	01.21.6038 / 02.01.07.328	
FUR/SDR	1.10	
OS	Windows 2008 Enterprise R2 64bit SP1	
Test Tool	iWVSS 2.5.3, SELViewer, Pvmode2, Syscfg, WinPIRA, MemPuller	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD Hot
4. Memory Stress		HVDD Cold
5. Memory Stress		LVDD Hot
6. Memory Stress		LVDD Cold
Note:		



AVL USE ONLY:							
Completed by:	Andy Chang	Completion Date:	03/10/15	AVL A#	A10433	AVL W/O	WD3190
Comments:							

Test Results

4C					
Minimum Loading					
Start Date		3/5/2015			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	SV8998	P	P	P	P
CPU1 A2					
CPU1 B1	SV8999	P	P	P	P
CPU1 B2					
CPU1 C1	SV9000	P	P	P	P
CPU1 C2					
CPU1 D1	SV9001	P	P	P	P
CPU1 D2					
CPU2 E1	SV9002	P	P	P	P
CPU2 E2					
CPU2 F1	SV9003	P	P	P	P
CPU2 F2					
CPU2 G1	SV9004	P	P	P	P
CPU2 G2					
CPU2 H1	SV9032	P	P	P	P
CPU2 H2					

4C					
Maximum Loading					
Start Date		03/06/15			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	SV8992	P	P	P	P
CPU1 A2	SV8993	P	P	P	P
CPU1 B1	SV8994	P	P	P	P
CPU1 B2	SV8995	P	P	P	P
CPU1 C1	SV8996	P	P	P	P
CPU1 C2	SV8997	P	P	P	P
CPU1 D1	SV8998	P	P	P	P
CPU1 D2	SV8999	P	P	P	P
CPU2 E1	SV9000	P	P	P	P
CPU2 E2	SV9001	P	P	P	P
CPU2 F1	SV9002	P	P	P	P
CPU2 F2	SV9003	P	P	P	P
CPU2 G1	SV9004	P	P	P	P
CPU2 G2	SV9005	P	P	P	P
CPU2 H1	SV9006	P	P	P	P
CPU2 H2	SV9031	P	P	P	P

4C					
Minimum Loading					
Start Date		3/3/2015			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	SV8998	P	P	P	P
CPU1 A2					
CPU1 B1	SV8999	P	P	P	P
CPU1 B2					
CPU1 C1	SV9000	P	P	P	P
CPU1 C2					
CPU1 D1	SV9001	P	P	P	P
CPU1 D2					
CPU2 E1	SV9002	P	P	P	P
CPU2 E2					
CPU2 F1	SV9003	P	P	P	P
CPU2 F2					
CPU2 G1	SV9004	P	P	P	P
CPU2 G2					
CPU2 H1	SV9005	P	P	P	P
CPU2 H2					

4C					
Maximum Loading					
Start Date		03/05/15			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	SV8992	P	P	P	P
CPU1 A2	SV8993	P	P	P	P
CPU1 B1	SV8994	P	P	P	P
CPU1 B2	SV8995	P	P	P	P
CPU1 C1	SV8996	P	P	P	P
CPU1 C2	SV8997	P	P	P	P
CPU1 D1	SV8998	P	P	P	P
CPU1 D2	SV8999	P	P	P	P
CPU2 E1	SV9000	P	P	P	P
CPU2 E2	SV9001	P	P	P	P
CPU2 F1	SV9002	P	P	P	P
CPU2 F2	SV9003	P	P	P	P
CPU2 G1	SV9004	P	P	P	P
CPU2 G2	SV9005	P	P	P	P
CPU2 H1	SV9006	P	P	P	P
CPU2 H2	SV9007	P	P	P	P