Advanced Validation Labs, Inc.

17665B Newhope Street, Fountain Valley, CA 92708 (714) 435-2630

Intel PCSD Server Memory Compatibility Test Certificate

Test System: **Intel S2600CP (Canoe Pass)** Test Result: **Pass**

Leveraged System(s): N/A

RPXXXXXXXXXXXXXXXXX

Module Information

Rev. 03/01/2014

DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KVR16LE11/8I	UDIMM ECC	1.35V	8GB	1Gx72	1600	11	E	DR
DRAM Vendor	**DRAM Part Number**	**DRAM Density / Width / Date Code**			**Register Vendor / Rev.**		**DIMM Composition**		
Hynix	H5TC4G83BFR-PBA	4Gb	512Mx8bit	1444			(512Mx8)x2*72		

Leveraged Memory Modules

	Vendor		Type	Voltage	CL	Speed
1	Kingston	KVR16LE11K3/24I	UDIMM ECC	1.35V	11	1600
2	Kingston	KVR16LE11K4/32I	UDIMM ECC	1.35V	11	1600
3	Kingston	KVR16E11/8I	UDIMM ECC	1.5V	11	1600
4	Kingston	KVR16E11K3/24I	UDIMM ECC	1.5V	11	1600
5	Kingston	KVR16E11K4/32I	UDIMM ECC	1.5V	11	1600
6						

System Configuration

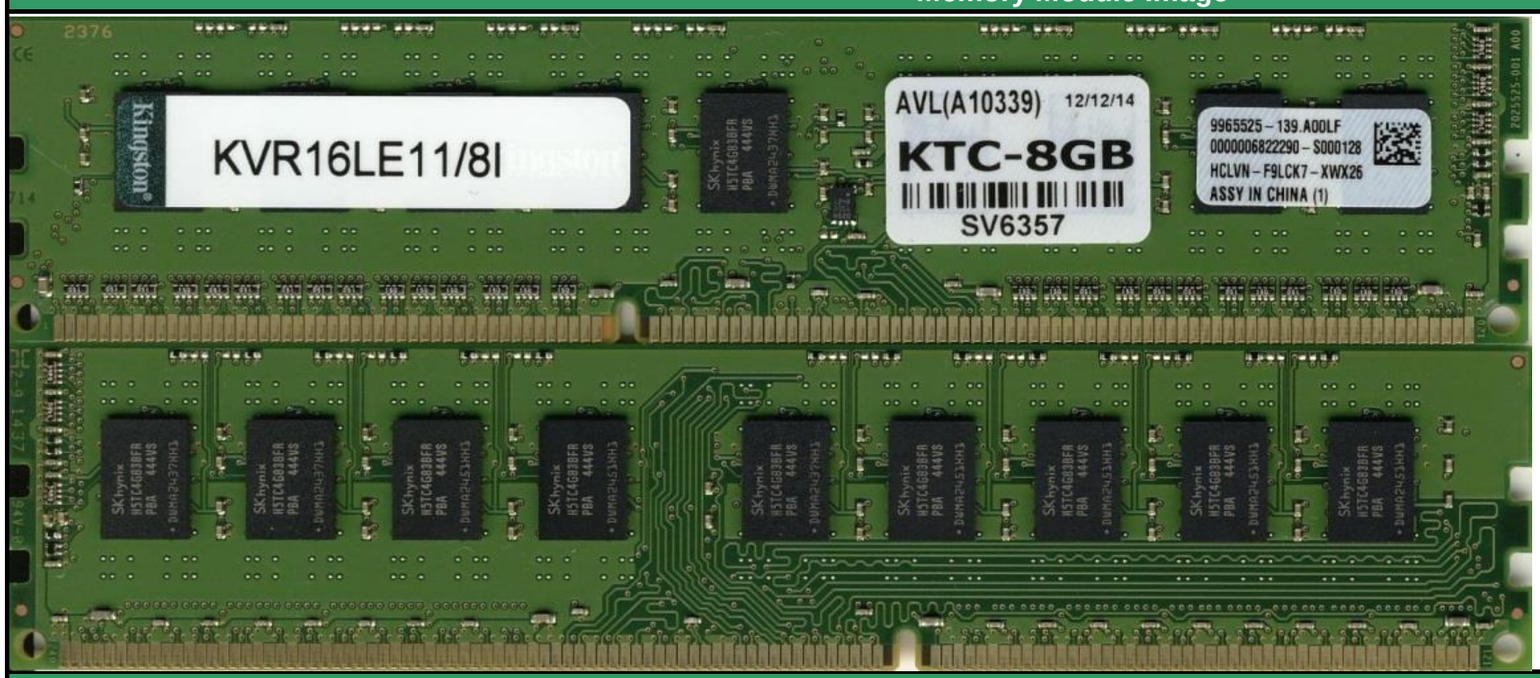
SETUP	System #1	System #2
AVL S/N	SN2311	SN6442
System S/N	QSCP13800398	QSCP14800221
Board Rev. (PBA)	E99552-401	G50768-501
CPU Type	E5-2680 v2 / 2.8 GHz	
Chipset	Intel C602	
BIOS / Date	02.03.0003 / 04/19/2014	
BMC / ME	01.21.6038 / 02.01.07.328	
FUR/SDR	1.10	
OS	Windows 2008 Enterprise R2 64bit SP1	
Test Tool	iWVSS 2.5.3, SELViewer, Pvmode2, Syscfg, WinPIRA,MemPuller	

Testing Summary

Test Items	Test Description		Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS		Done
2. SPD Check	DIMM SPD content check for JEDEC compliance		Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD Hot	Pass
4. Memory Stress		HVDD Cold	Pass
5. Memory Stress		LVDD Hot	Pass
6. Memory Stress		LVDD Cold	Pass

Note:

Memory Module Image



AVL USE ONLY:

Completed by:	Andy Chang	Completion Date:	01/21/2015	AVL A#	A10339		AVL W/O	WD2924

Comments:

Test Results

4C Minimum Loading					
Start Date		1/17/2015			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	SV6395	P	P	P	P
CPU1 A2					
CPU1 B1	SV6396	P	P	P	P
CPU1 B2					
CPU1 C1	SV6397	P	P	P	P
CPU1 C2					
CPU1 D1	SV6398	P	P	P	P
CPU1 D2					
CPU2 E1	SV6399	P	P	P	P
CPU2 E2					
CPU2 F1	SV63400	P	P	P	P
CPU2 F2					
CPU2 G1	SV6401	P	P	P	P
CPU2 G2					
CPU2 H1	SV6261	P	P	P	P
CPU2 H2					

4C Maximum Loading					
Start Date		11/17/15			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	SV6379	P	P	P	P
CPU1 A2	SV6380	P	P	P	P
CPU1 B1	SV6381	P	P	P	P
CPU1 B2	SV6382	P	P	P	P
CPU1 C1	SV6383	P	P	P	P
CPU1 C2	SV6384	P	P	P	P
CPU1 D1	SV6385	P	P	P	P
CPU1 D2	SV6386	P	P	P	P
CPU2 E1	SV6387	P	P	P	P
CPU2 E2	SV6388	P	P	P	P
CPU2 F1	SV6389	P	P	P	P
CPU2 F2	SV6390	P	P	P	P
CPU2 G1	SV6391	P	P	P	P
CPU2 G2	SV6392	P	P	P	P
CPU2 H1	SV6293	P	P	P	P
CPU2 H2	SV6260	P	P	P	P

4C Minimum Loading					
Start Date		1/14/2015			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	SV6395	P	P	P	P
CPU1 A2					
CPU1 B1	SV6396	P	P	P	P
CPU1 B2					
CPU1 C1	SV6397	P	P	P	P
CPU1 C2					
CPU1 D1	SV6398	P	P	P	P
CPU1 D2					
CPU2 E1	SV6399	P	P	P	P
CPU2 E2					
CPU2 F1	SV63400	P	P	P	P
CPU2 F2					
CPU2 G1	SV6401	P	P	P	P
CPU2 G2					
CPU2 H1	SV6402	P	P	P	P
CPU2 H2					

4C Maximum Loading					
Start Date		01/15/15			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	SV6379	P	P	P	P
CPU1 A2	SV6380	P	P	P	P
CPU1 B1	SV6381	P	P	P	P
CPU1 B2	SV6382	P	P	P	P
CPU1 C1	SV6383	P	P	P	P
CPU1 C2	SV6384	P	P	P	P
CPU1 D1	SV6385	P	P	P	P
CPU1 D2	SV6386	P	P	P	P
CPU2 E1	SV6387	P	P	P	P
CPU2 E2	SV6388	P	P	P	P
CPU2 F1	SV6389	P	P	P	P
CPU2 F2	SV6390	P	P	P	P
CPU2 G1	SV6391	P	P	P	P
CPU2 G2	SV6392	P	P	P	P
CPU2 H1	SV6293	P	P	P	P
CPU2 H2	SV6394	P	P	P	P