Advanced Validation Labs, Inc.
17665B Newhope Street, Fountain Valley, CA 92708 (714) 435-2630

Intel PCSD Server Memory Compatibility Test Certificate

Test System: **Intel S2600CP (Canoe Pass)** Test Result: **Pass**

Leveraged System(s): S2600WP Washington Pass; W2600CR Crown Pass; S2400SC Swiftcurrent Pass; S2400LP Lincoln Pass; S2600KI Kings Island; S1600JP Jackson Pass; S2400EP Eagle Pass; S2400BB Black Bear Pass; S1400FP Freemont Pass; S1400SP Salmon Pass; S2600IP Iron Pass; S2600CO Copper Pass.

Module Information

DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KVR16LE11S8/4I	UDIMM ECC	1.35V	4GB	512Mx72	1600	11	D	SR
DRAM Vendor	**DRAM Part Number**	**DRAM Density / Width / Date Code**			**Register Vendor / Rev.**			**DIMM Composition**	
Hynix	H5TC4G83DFR-PBA	4Gb	512Mx8bit	1637				(512Mx8)*72	

Leveraged Memory Modules

	Vendor		Type	Voltage	CL	Speed
1						
2						
3						
4						
5						
6						

System Configuration

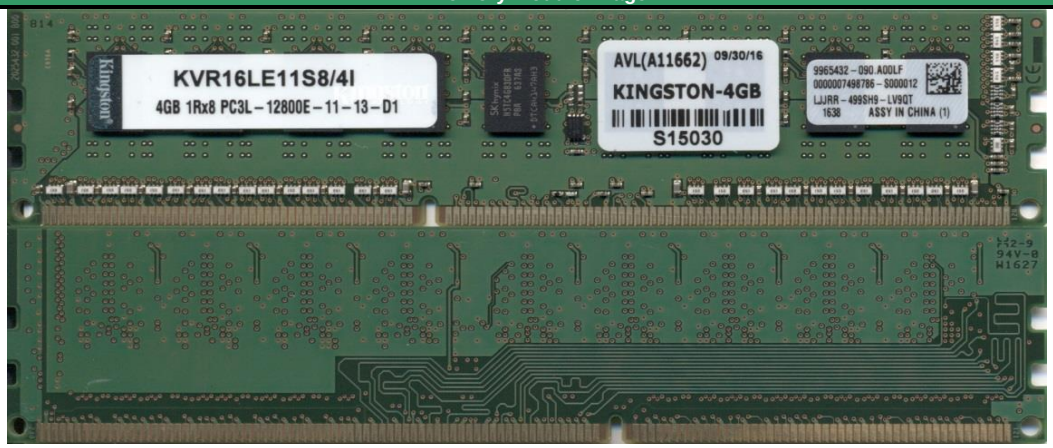
SETUP	System #1	System #2
AVL S/N	SN2311	SN6442
System S/N	QSCP13800398	QSCP14800221
Board Rev. (PBA)	E99552-401	G50768-501
CPU Type	E5-2680 v2 / 2.8 GHz	
Chipset	Intel C602	
BIOS / Date	02.03.0003 / 04/19/2014	
BMC / ME	01.21.6038 / 02.01.07.328	
FUR/SDR	1.11	
OS	Windows 2008 Enterprise R2 64bit SP1	
Test Tool	iWVSS 2.6.5, SELViewer, Pvmode2, Syscfg, WinPIRA,MemPuller	

Testing Summary

Test Items	Test Description		Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS		Done
2. SPD Check	DIMM SPD content check for JEDEC compliance		Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD Hot	Pass
4. Memory Stress		HVDD Cold	Pass
5. Memory Stress		LVDD Hot	Pass
6. Memory Stress		LVDD Cold	Pass

Note:

Memory Module Image



AVL USE ONLY:

Completed by:	Andy Chang	Completion Date:	02/08/2017	AVL A#	A11662		AVL W/O	WD6991

Comments:

Test Results

4C Minimum Loading					
Start Date		11/7/2016			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	S16886	P	P	P	P
CPU1 A2					
CPU1 B1	S16887	P	P	P	P
CPU1 B2					
CPU1 C1	S16888	P	P	P	P
CPU1 C2					
CPU1 D1	S16889	P	P	P	P
CPU1 D2					
CPU2 E1	S16890	P	P	P	P
CPU2 E2					
CPU2 F1	S16891	P	P	P	P
CPU2 F2					
CPU2 G1	S16892	P	P	P	P
CPU2 G2					
CPU2 H1	S16893	P	P	P	P
CPU2 H2					

4C Maximun Loading					
Start Date		11/2/2016			
DIMM Voltage		1.5v			
DIMM	S/N	A	B	C	D
CPU1 A1	S16886	P	P	P	P
CPU1 A2	S16887	P	P	P	P
CPU1 B1	S16888	P	P	P	P
CPU1 B2	S16889	P	P	P	P
CPU1 C1	S16890	P	P	P	P
CPU1 C2	S16891	P	P	P	P
CPU1 D1	S16892	P	P	P	P
CPU1 D2	S16893	P	P	P	P
CPU2 E1	S16894	P	P	P	P
CPU2 E2	S16895	P	P	P	P
CPU2 F1	S16896	P	P	P	P
CPU2 F2	S16897	P	P	P	P
CPU2 G1	S16898	P	P	P	P
CPU2 G2	S16899	P	P	P	P
CPU2 H1	S16900	P	P	P	P
CPU2 H2	S16901	P	P	P	P

4C Minimum Loading					
Start Date		11/9/2016			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	S16886	P	P	P	P
CPU1 A2					
CPU1 B1	S16887	P	P	P	P
CPU1 B2					
CPU1 C1	S16888	P	P	P	P
CPU1 C2					
CPU1 D1	S16889	P	P	P	P
CPU1 D2					
CPU2 E1	S16890	P	P	P	P
CPU2 E2					
CPU2 F1	S16891	P	P	P	P
CPU2 F2					
CPU2 G1	S16892	P	P	P	P
CPU2 G2					
CPU2 H1	S16893	P	P	P	P
CPU2 H2					

4C Maximun Loading					
Start Date		11/4/2016			
DIMM Voltage		1.35v			
DIMM	S/N	E	F	G	H
CPU1 A1	S16886	P	P	P	P
CPU1 A2	S16887	P	P	P	P
CPU1 B1	S16888	P	P	P	P
CPU1 B2	S16889	P	P	P	P
CPU1 C1	S16890	P	P	P	P
CPU1 C2	S16891	P	P	P	P
CPU1 D1	S16892	P	P	P	P
CPU1 D2	S16893	P	P	P	P
CPU2 E1	S16894	P	P	P	P
CPU2 E2	S16895	P	P	P	P
CPU2 F1	S16896	P	P	P	P
CPU2 F2	S16897	P	P	P	P
CPU2 G1	S16898	P	P	P	P
CPU2 G2	S16899	P	P	P	P
CPU2 H1	S16900	P	P	P	P
CPU2 H2	S16901	P	P	P	P