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S1200BT Bearthooth Memory Compatibility Test Report

Completed by:	Mike H.	TEMPERATURE AND VOLTAGE SETTINGS				
Completion Date:	4/3/2015	Corner	Temp (°C)	CPU 1 Modify DRAM 1.5v to	CPU 0 Modify DRAM 1.5v to	Duration
AVL WorkOrder #	WD2919	A	55°C	1.55v	1.55v	6 hrs.
AVL A#	A10338	B	0°C	1.55v	1.55v	6 hrs.
		C	55°C	1.45v	1.45v	6 hrs.
		D	0°C	1.45v	1.45v	6 hrs.

Memory Modules Tested

Vendor	DIMM P/N / Type	UDIMM ECC	Voltage	Size	Density	Speed	CL	R/C	Rank	
Kingston	KVR16LE11S8/4I		1.35v	4GB	512Mx72	1600	11	D	1	
DRAM Vendor / P/N / Date Code		DRAM Density / Width		Register Vendor / Rev.		DIMM Composition				
Hynix	H5TC4G83BFR-PBA	1444	4Gb	512Mx8bit			(512Mx8)*72			

Leveraged Memory Modules

Vendor	Type	Voltage	CL	Speed
1 Kingston	KVR16E11S8/4I	UDIMM ECC	1.5v	11 1600
2				
3				

System Configuration

SETUP	Red	Blue
AVL S/N	SP4661	SP2701
S/N	QSBT21308879	QSBT20800317
PBA	E98683-352	E98681-351
BMC	01.17.5256	
BIOS	S1200BT.86B.02.01.0041	
SDR	1.16	
OS	Win2K8 Enterprise 64bit	
Test Tool	iWVSS 2.0.1, SELViewer, PvMode tool, PIRA	
CPU 1	Type	
	E31245 v2 / 3.40GHz	E31245 v2 / 3.40GHz
	206a7	206a7

Test Results

4C						
Minimum Loading						
Start Date		3/9/2015				
DIMM	S/N	A	B	C	D	
CPU1 A2						
CPU1 A1	SV6232	P	P	P	P	
CPU1 B2						
CPU1 B1	SV6233	P	P	P	P	
AC/DC Power Cycling						
50 AC Cycles/corner			P			P
50 DC Cycles/corner			P			P

4C						
Mid Loading						
Start Date		3/20/2015				
DIMM	S/N	A	B	C	D	
CPU1 A2	SV6232	P	P	P	P	
CPU1 A1	SV6233	P	P	P	P	
CPU1 B2	SV6234	P	P	P	P	
CPU1 B1	SV6235	P	P	P	P	
AC/DC Power Cycling						
50 AC Cycles/corner			P			P
50 DC Cycles/corner			P			P

Module Image

