

	AVL Supermicro server platform Memory Module Qualification Test		
	Intel X5650(WSM) x 2, Intel 5520 (Tylersburg), Rev B3	Test Results	Pass
	PN: KVR1333D3S8R9S/2GHB (2GB /RDIMM / ECC) On: X8DT6-F Rev.1.3		

RP77D3x-106-KI-SQ-SMC-V1		Module Information		Rev 01/07/2011
AVL WorkOrder #	WB8324	AVL A#	6517	
Start Date	1/5/2011	End Date		
Tested By		Mike H.		
Module Manufacturer		Kingston		
Module Part Number		KVR1333D3S8R9S/2GHB		
Module BOM Number		9965439-026.A00LF		
Module Capacity / Memory Type / ECC		2GB /RDIMM / ECC		
Module Configuration (Width, # of devices, # of Ranks)		256Mx72 /9 Devices / 1Rank		
Speed Tested (Data rate of Mbps, CL-tRP-tRCD)		DDR3-1333 /9-9-9		
DRAM Device Vendor		Hynix		
DRAM Device Part Number / Date code		H5TQ2G83BFR-H9C		1049
DRAM Die Revision / Process Technology (nm)		F		
DRAM Device Config (Density / Width)		2Gbit / x8 / 256Mx8bit		
Thermal Sensor Device Vendor / Part Number / Revision				
Register Device Vendor / Part Number / Revision		TI TE32882E-G1		

(Module image)



Platform System Information				
Motherboard Info (Model# & MB Revision & MB S/N & AVL S/N)	X8DT6-F	1.3	VMAS59161	SK4375
BISO Revision / BIOS Date / AMIBIOS / MRC Rev.	2.0a	9/14/2010	NA	2.11
CPU / Speed	Intel X5650(WSM) x 2		2.66GHz	
Chipset info (Stepping)	Intel 5520 (Tylersburg), Rev B3			



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Test Results:

PASS

Comments:

AVL Memory Module Qual Test Results Summary

Test # and name	Test Description	Specs	Test	Comments
			Results	
1. Latest BIOS Upgrade & Configuration	Download / Upgrade latest BIOS & record size and speed detection	Per test platform, DIMM & config spec	Done	Record memory size & speed at each test only
2. SPD Check	Memory module SPD content check for JEDEC compliance	JEDEC	Pass	Use proprietary tools
3. Sisoftware Sandra Benchmark	Run Windows based diags & utility software @55°C - DIMM max loading.	1 loop per config	Done	Full load per spec
4. Passmark Burn-In		12 Hour per config	Pass	Full load per spec
5a. Stress Application Test	Run Linux based diags & utility software @55°C - DIMM max loading.	12 Hour per config	Pass	Full load per spec
5b. Stream Benchmark Test		5 loop per config	Done	Full load per spec
5c. Reset Cycle		500 loop per config	Pass	Full load per spec
6a. Functional Stress Test (Corner 1a)	Run RST Premium @55°C - DIMM max loading.	8 Hour or 2+ Loops per config	Pass	Full load per spec
6b. Functional Stress Test (Corner 1b)*	Run RST Premium @55°C - DIMM loading depends on @speed configuration	8 Hour or 2+ Loops per config	Pass	Per platform memory speed configuration table if different (run @Max module speed)
7. Functional Stress Test (Corner 2)	Run RST Premium @0°C DIMM max loading	8 Hour or 2+ Loops per config	Pass	Full load per spec

Note: All test under IMC Vdd=Nom, Vref=Vddnom/2

* Corner 1b test is not required if Corner 1A already covers max module speed.